

CoreTek 克達科技

Wide Bandgap Semiconductor testing challenge on SiC / GaN






2022 

 禧恩科技股份有限公司
POMME TECHNOLOGIES CO.,LTD

CoreTek 克達科技

Agenda

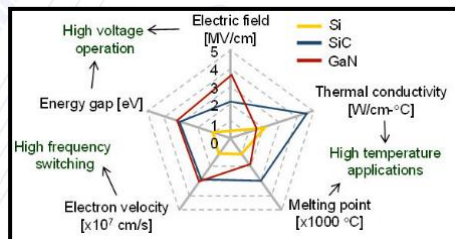
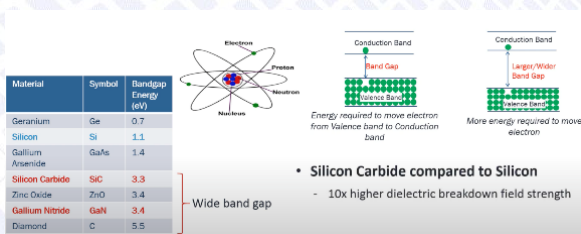
- WBG semiconductor advantage
- WBG device test challenge
- Power loss/ Current Collapse/ DPT testing
- POMME WBG fitted solutions
- CoreTech/ POMME introduce and WBG Solutions



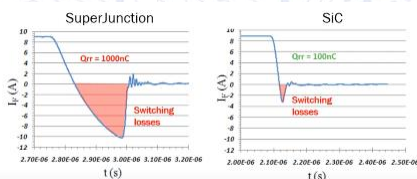



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WBG semiconductor advantage

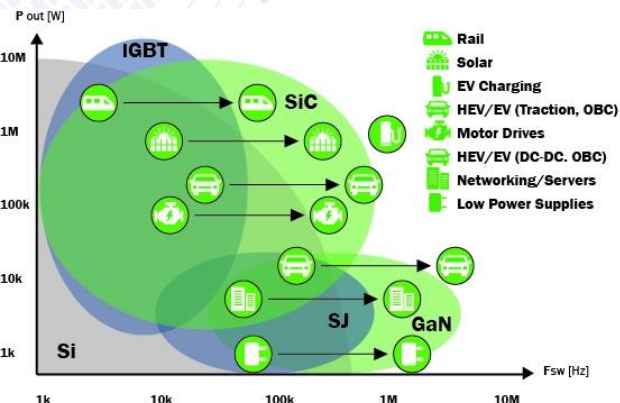
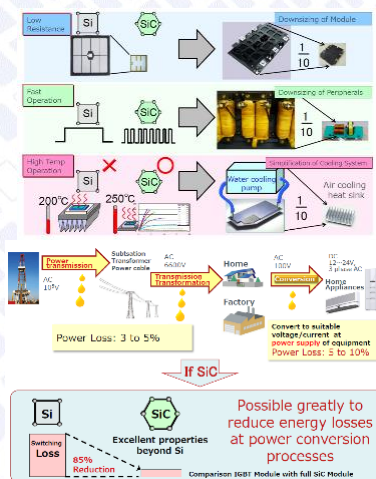


- ❑ Low DC power loss
- ❑ Higher Operate I/V
- ❑ Higher Frequencies
- ❑ Higher Thermo-stability



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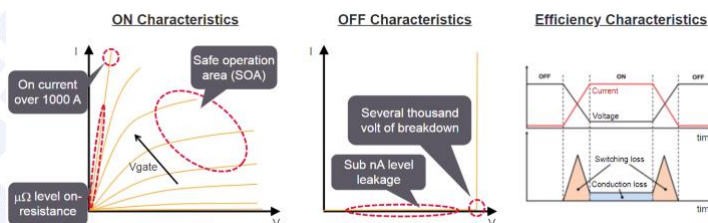
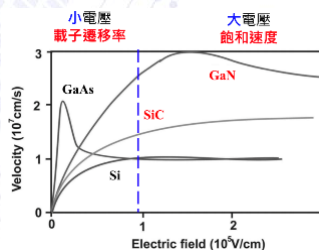
WBG Market Position



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WBG device test challenge

- Power loss(Ron/CV/Qg)
- Current collapse(GaN)
- High DC bias capacitance(GaN)
- DPT testing
- Thermo-testing



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Traditional Test Equipment Weakness

- Poor IV accuracy, non-CV/ Qg/ GaN C.C.
- Hard to prevent self-heating
 - Wide Pulse Width
- No well prepare test fixture
 - Rewire instrument connection
 - Wafer → Die → PKG → Module
- Thermal testing limit
 - Long cabling
 - Hard temperature monitor
 - Thermal chuck as a test pad(Vertical DUT)
- Wafer prober auto-testing
- No switching solution for HV/HC
- WBG DPT solution lack: Higher V/C, Higher speed



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Power loss

Total Loss = Conduction Loss + Switching Loss + Driving Loss

Key Parameters: R_{on} R_g, C_{rss}, C_{oss} Q_g

Drive Loss
 $= f \cdot Q_g \cdot V_{gs}$

Switching Loss
 $\propto f \cdot (V \cdot I \cdot \Delta T)$

Conduction Loss
 $= R_{on} \cdot (I_{RMS})^2$

Ron

Rg/CV

Qg

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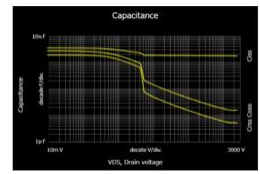
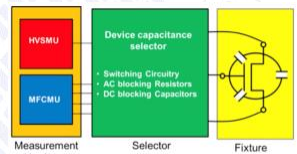
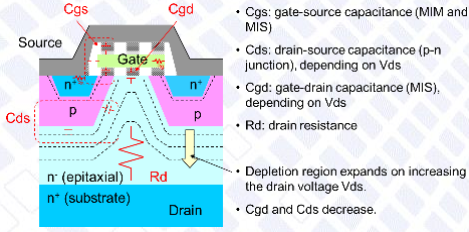
On-Resistance: Rds(on)

- ❑ WBG Rds(on) is very low to conductive loss can be ignored.
- ❑ How to measure uOhm Rds(on) is the key:
 - ① Specified test fixture with low R_s
 - ② High current affordable
 - ③ Suitable R_g and duty cycle on Gate/Drain to prevent DUT broken!

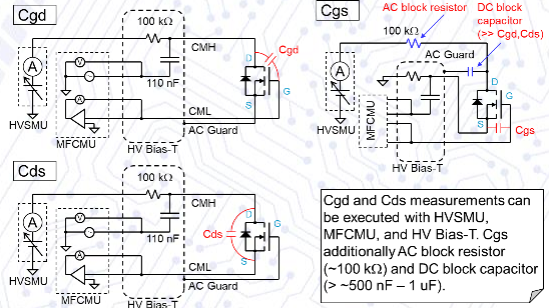
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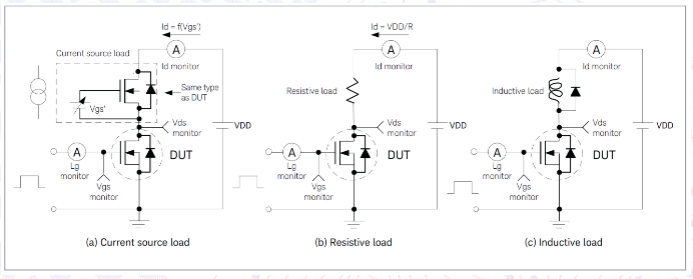
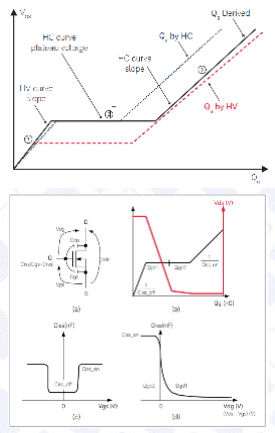
Capacitor: Ciss/ Coss/ Crss



Ciss: Input Capacitance
 $C_{iss} = C_{gs} + C_{gd}$
 Coss: Output Capacitance
 $C_{oss} = C_{ds} + C_{gd}$
 Crss: Reverse Transfer Capacitance
 $C_{rss} = C_{gd}$



Gate charge: Qg



Qg measurement difficult

- A stable power supply to provide accurate time dependent output voltage and current.
- A gate drive circuit which can accurately measure time dependent current and voltage.

GaN C.C.: Current Collapse

1 On-state $V_G = 0V$, $V_D > 0$

2 Off-state $V_G < 0V$, $V_D >> 0$

Carrier depletion region

3 On-state (current collapse) $V_G = 0V$, $V_D > 0$

Carrier depletion region

Drain current at higher VDD is smaller than it at lower VDD!?

- On resistance changes dynamically after changing from off state to on state.
- On resistance is depending on VDD and duration of off-state.
- Caused by the same mechanism with the current collapse phenomena observed at IV measurement.

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DPT testing

WBG DPT solution:

- ✓ Higher V/C
- ✓ Higher speed
- ✓ Specified fixture

Double pulse testing:

- Switching
- Reverse recovery
- UIS(Unclamped inductive switching)
- Gate charge

Switching waveform

Diode reverse recovery waveform

UIS waveform

Gate charge waveform

Turn-on delay time	td(on)
Rise time	tr
Turn-off delay time	td(off)
Fall time	tf

Reverse recovery time	trr
Reverse recovery charge	Qrr
Peak reverse recovery current	Irrm

Avalanche current	Ias
MOSFET dv/dt ruggedness(UIS/EAS)	dv/dt
Reverse diode dv/dt	dv/dt

Gate to source charge	Qgs
Gate to drain charge	Qgd
Gate charge total	Qg
Gate plateau voltage	Vplateau

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Coretek / POMME introduce

克達科技/禧恩科技提供了完整的第三代半導體所要求的規格的測試方案：

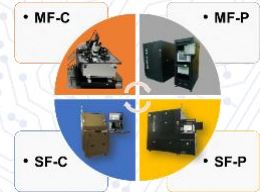
- DC Power Device Parameter 測試方案
- AC Power Device parameter 測試方案
- Low current device characterization parameter 測試方案

針對寬能隙WBG(Wide Band Gap)Device 設計了加熱器· I/V Curve · C/V Curve 及On-wafer 測試探針及密制的治具· 提供一站式的解決方案· 大幅減輕您測試環境的架設及測試時間。

針對On-wafer 測試· 克達科技提供您完整測試方案從手動· 半自動到全自動等· 探針/探針卡全面性的解決方案

1. Manual: MF-P/ MF-C series
2. Semi-Auto: SF-P/ SF-C series
3. Fully-Auto: IF series

Probe Station	Tester	Turn-Key System	OST service	Instrument Service
Type ➢ Manual ➢ Semi-auto ➢ Fully-auto	DC ➢ Keysight ➢ Keithley ➢ Owned PXI	RDL tester 4PP Tester TEG Tester Reliability Package	Wind Band Gap Low Current Reliability Burn-In	Calibration Loan Repair Used Resell
Function ➢ Low Current/ WBG ➢ LCD/Vacuum	AC ➢ Manual DPT ➢ Surge Current Tester			

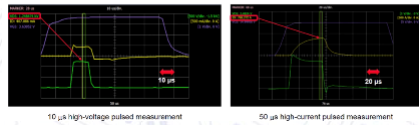


WBG fitted solutions – 1

- **Great IV accuracy, non-CV/ Qg/ GaN C.C.**
 - B1505A/ B1506A
- **Hard to prevent self-heating**
 - POMME provide turnkey 10 us level testing
- **No well prepare test fixture**
 - Rewire instrument connection
 - Wafer → Die → PKG → Module



- Shorter pulses are always better (10 μ s in this example)
- It is useful to have some means to visually verify the pulsed waveforms



WBG fitted solutions - 2

- **Thermal testing limit**
 - New high-temp test board
 - Thermal couple for temp-monitor
 - ▣ **Wafer/Die testing:** 3kV/ 10kV/ 300A thermal chuck(-55~300 °C)
 - ▣ **Discrete/SMD:** Thermostream(-80~225 °C)
 - ▣ **Power Module:** Thermal plate(RT~250 °C)
- **Wafer prober auto-testing**
 - Fully configured probing tips between HV/HC
 - Owned design semiauto/ auto prober
- **No switching solution for HV/HC**
 - Module selector: N1258&9A/ N1265A/ B1506A

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Keysight B1505A/ B1506A

For Power Semiconductor Manufacturers – B1505A

- Very wide range up to 1500 A / 10 kV
- Covering CV, Qg and GaN current collapse measurements
- Supporting both on-wafer and packaged devices
- Automated IV, CV, thermal test capability from -50 °C to +250 °C
- Safe and easy to use

Power Devices
(Wafer)

Power Devices
(Discrete)

Power Modules

Power Devices in
Inverter/ Converter

Power Devices in
Electric Circuit

For power electronics engineers using power devices in their products – B1506A

- Fully automated IV, CV and semi-automatic Qg measurements
- Fully automated IV / CV thermal test from -50 °C to +250 °C
- Power loss calculation using IV, CV, Rg, Qg
- Up to 1500 A / 3 kV
- Safe, simple and easy to use

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AC: ipTEST DS5

ABOUT ipTEST

ipTEST has become the leading manufacturer of high-speed power discrete test systems to the worlds largest Integrated Device Manufacturers (IDM's)

- We exclusively focus on developing high speed production-line testers
- We have 35 years of high-power discrete testing know-how
- We have more than 500 test systems sold worldwide
- We are flexible to customer demands and deliver highly customized systems
- Our engineering support and R&D teams are openly accessible to customer engineers
- Located in UK, 30km south-west of London

Single & Double Pulse Test

- On/Off times
- Rise/Fall times
- Switching Energy

Diode Recovery

- Tr_r, I_{rr}, Q_{rr}

Short Circuit

- I_{sc}

On Off On

Measure Vd and Id here to calculate Dynamic RDSon

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WBG Manual Probe Station

- Model MF-PE2000 is for 8" wafers.
- Model MF-PE3000 is for 12" wafers.

- Applications
 - Temperature characteristics tests in range from +20°C to +300°C
 - Ultra low signal I-V measurements (100fA level)
 - Various C-V measurements (quasi-static C-V, HF-CV, and RF-CV)
 - RF measurements (up to 67 GHz)
 - Ultra high-speed I-V measurements
- Extend Application
 - High-power device measurements
(=300A pulse, ±3kV triaxial, ±10kV coaxial)
 - Wafer level reliability tests (such as EM, TDDB, HCI, NBT1, and BT)
 - Probe card support (support Multi-site WLR)
 - Built-in laser cutter (Point marking, Exfoliation of protection layer, Metal layer cutting)
 - Active vibration isolator and ultra high-accuracy probing through image processing pattern recognition (accuracy: ±1 um or finer)
 - Light-receiving/emitting characteristics evaluation applications for optoelectronics (such as LED, LD, VCSEL, and PD)
 - Common gate pad contacts of flat-panel display devices

	MF-P2000	MF-P3000
Wafer Size	~ φ100 mm	~ φ150 mm
X-Y travel	X:105mm · Y:200mm	X:160mm · Y:250mm
X-Y repeatability	< ±2μm	
X-Y accuracy	< ±5μm	
Z travel	20mm	
Z repeatability	< ±1μm	
θ travel	±7.5°	
θ repeatability	0.002°	
Unit dimension(WxDxH)*	1200x900x1550mm	1250x900x1550mm
Weight*	650kg (depending on configuration)	700kg

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