

工業4.0加速器

智慧工業市場暨技術發展研討會

TOSHIBA

Toshiba Electronic Components Taiwan Corporation

Hitoshi Mizunuma 水沼 仁志

2019.12.12

Acceleration of industry 4.0 business growth

2017 Dec. 12th

2019 Dec. 12th



tech Taipei
for top electronics engineers

智慧製造新時代 工業4.0展開高效能革命

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智慧製造新時代 工業4.0展開高效能革命

在德國率先提出工業4.0(Industry 4.0)概念後，全球先進製造國家亦前仆後繼搶佔此競爭舞台，日漸增長的工業物聯網(IoT)趨勢，正改變著整個工業領域，如何在選擇高效數據傳輸系統的同時，讓工業領域更加優化、高效地運行，實現智慧製造、智慧工廠的願景，是今日我們都急於探知的目標。

工業4.0與智慧工廠研討會

時間: 2017年12月12日

地點: 華南銀行總行大樓會議中心

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2019智慧工業市場暨技術發展研討會
2019年12月12日·華南銀行國際會議中心
2019年12月19日·台北富邦國際會議中心

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2019年5G商用開台啟動，該技術高傳輸、低延遲與更廣的覆蓋範圍等特性，使其適用於各種產業。其中，工業市場在工業4.0概念的推動下，也將陸續導入各式新技術，包括人工智慧(AI)、自動光學檢測(AOI)、機器學習、機器視覺、遠端即時操控...等，這些新技術也須藉助5G強悍，才能滿足工業應用環境的嚴苛要求，進一步落實「智慧製造」。為協助相關工程師深入瞭解5G工業物聯網、智慧製造...等市場與技術發展，TechTaipei邀集業界重量級廠商Adesto、Apacer、Bluetooth、Burnon、G4、Keysight、Maxim、NXP、Rohm、Renesas、Silicon Labs、ST、Toshiba、T-Global、Winbond.....等與會，並提供深度交流機會。

- 2019年12月12日(星期四)華南銀行國際會議中心
- 2019年12月19日(星期四)台北富邦國際會議中心

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多項好禮+現場獨家加碼獎，讓您和尖端技術一起帶回家

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時間: 2019年12月12日(星期四)

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智慧工業市場暨技術發展研討會

時間: 2019年12月19日(星期四)

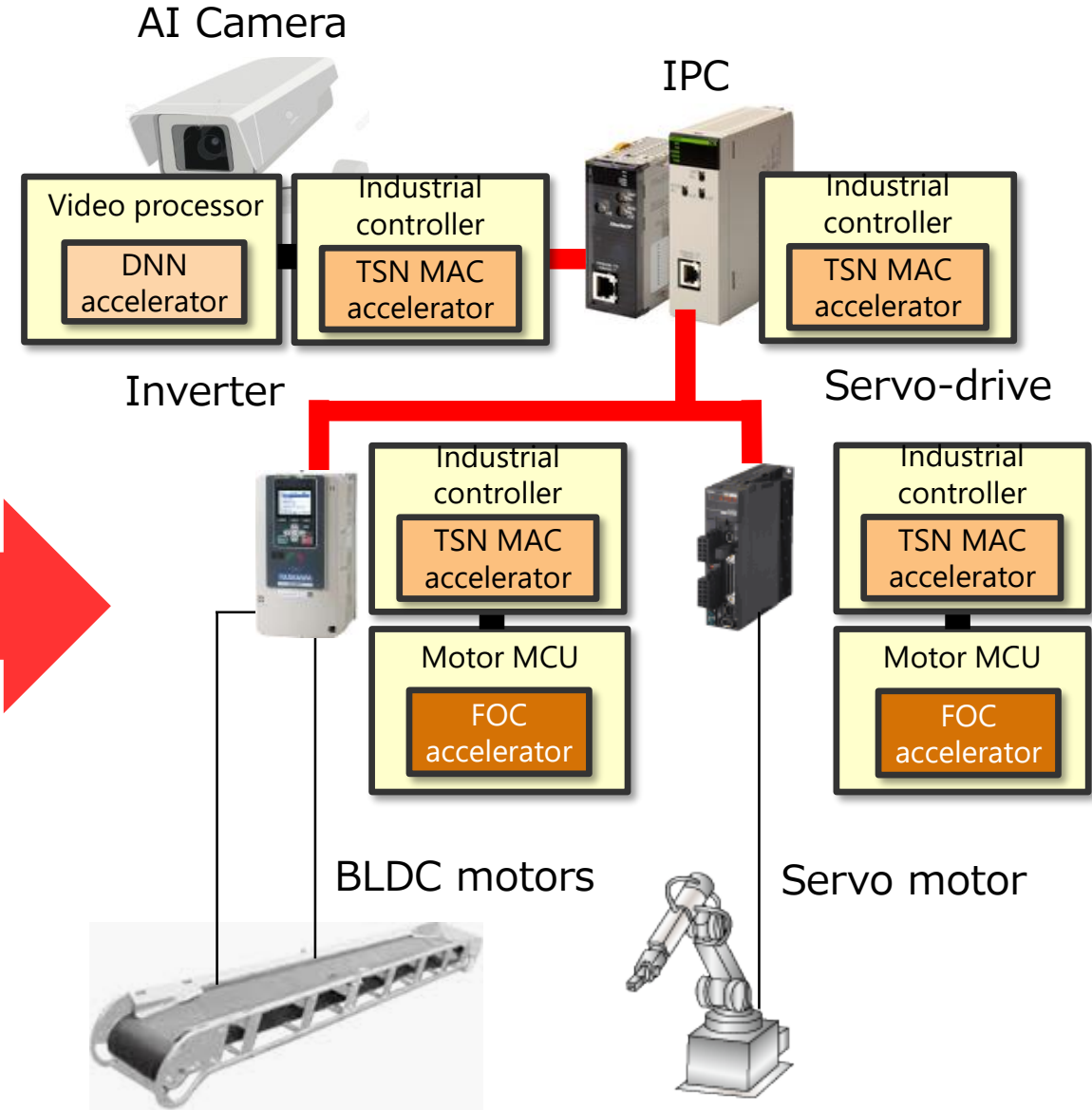
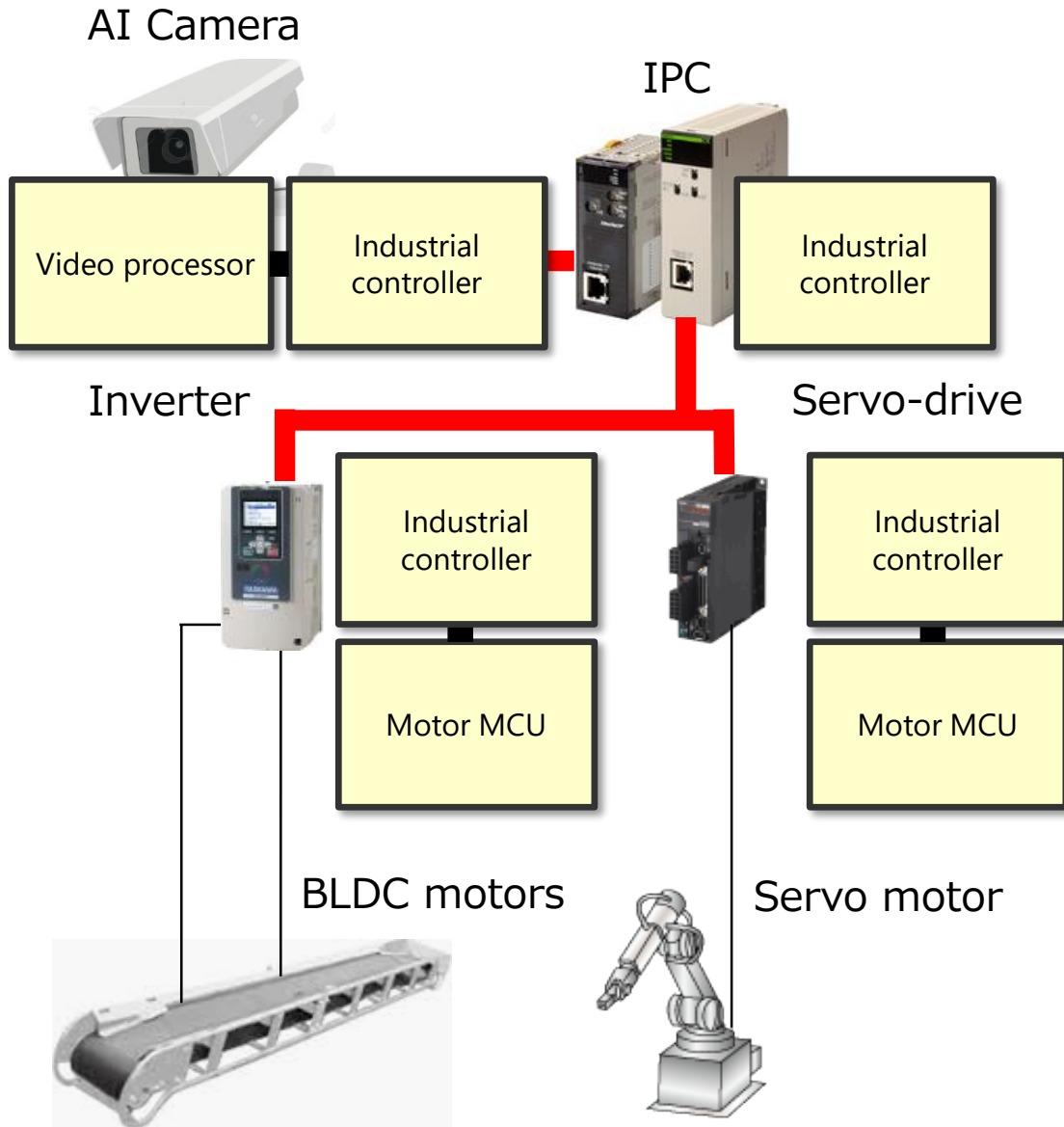
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Acceleration of industry 4.0 operation

ICs

Toshiba unique IP
Industrial Ethernet

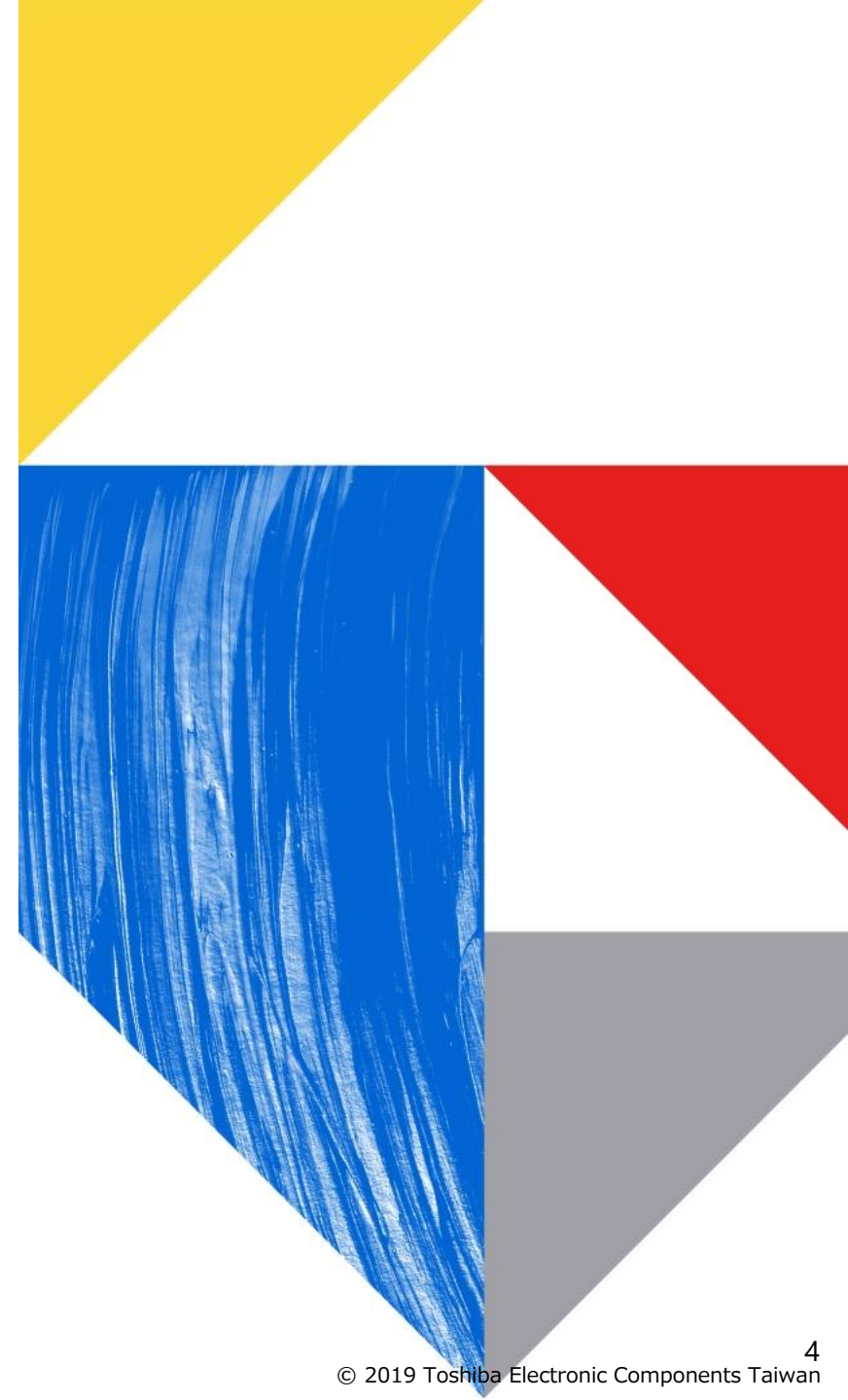


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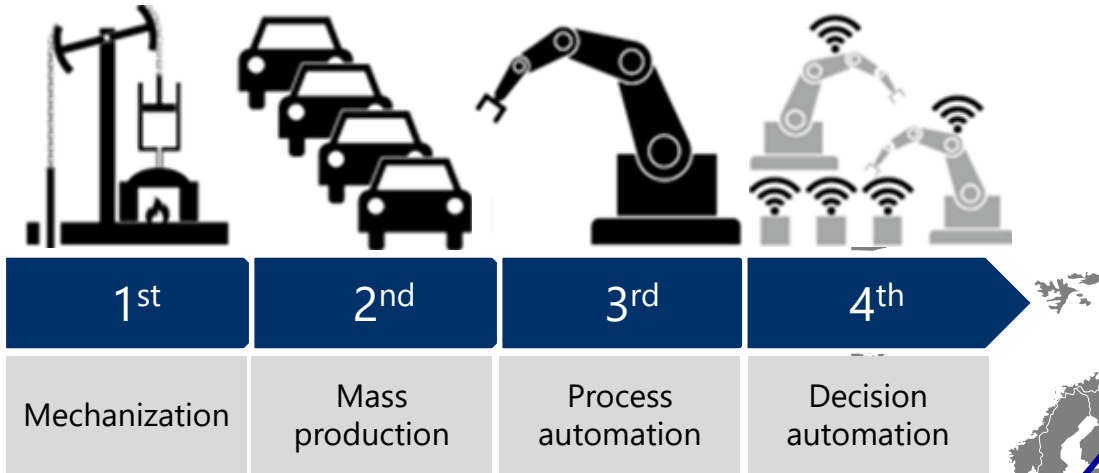
- 01 工業4.0現況
- 02 工業4.0加速器I “FOC accelerator”
- 03 工業4.0加速器II “TSN accelerator”
- 04 工業4.0加速器III “DNN accelerator”
- 05 示範平臺


01

工業4.0現況




Leading countries of the industrial market



Industries 4.0 

- Integrated industrial machinery etc on the CPS (Cyber-Physical System) through the network
- Flexible production according to mass customization
- Cooperation of humans and robots

Industrial Internet 

- Connecting "Industrial equipment" and "Internet", greatly evolving industrial equipment

Advanced Manufacturing

- 3D printer, digital manufacturing, material development, robot engineering, etc.

EFFRA (European Factories of the Future Research Association) 

HVM (High Value Manufacturing) 

France New industries (La Nouvelle France Industrielle) 

Make In India with IoT Policy 

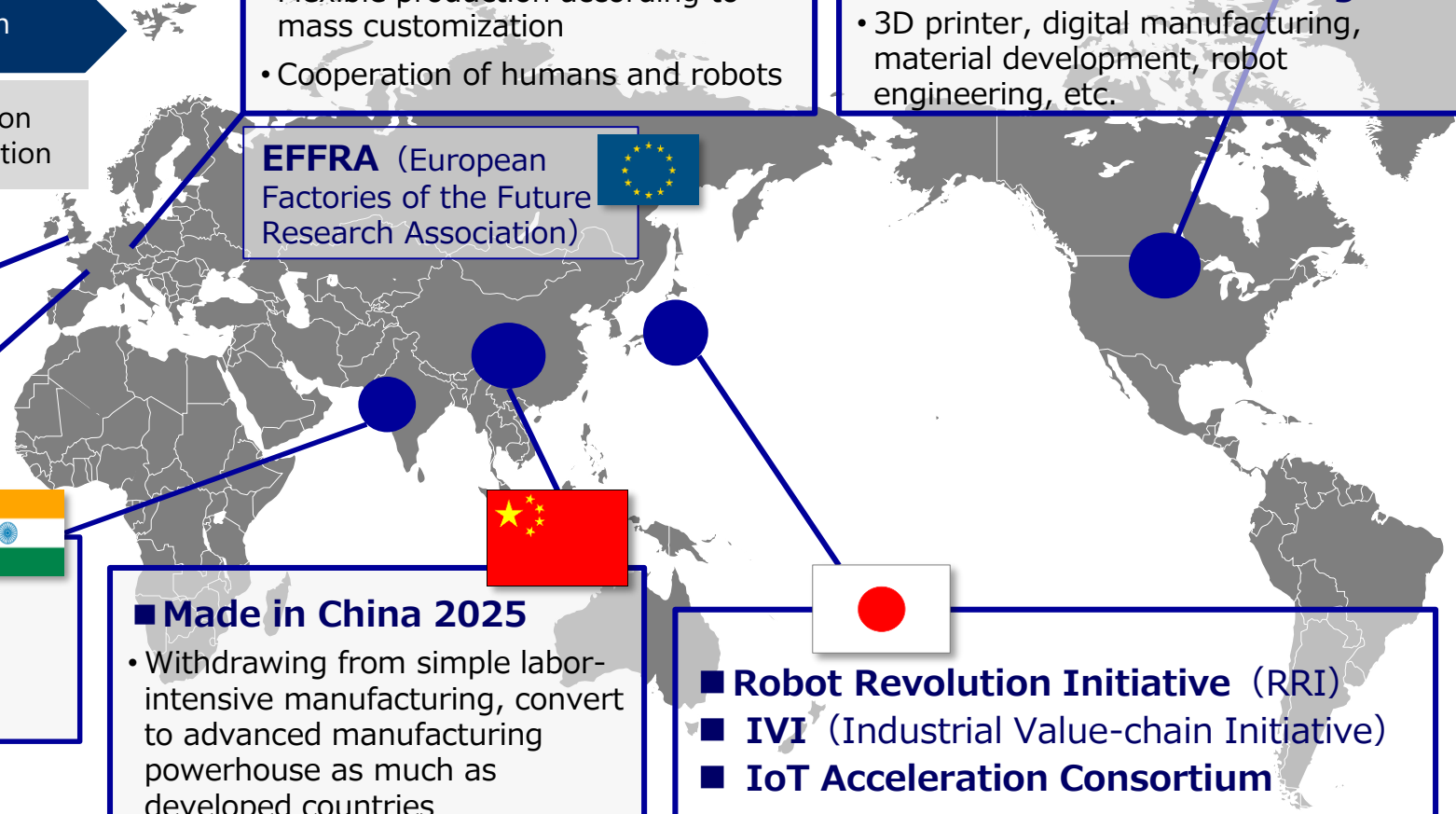
- As Make in India is attracting factories, utilize the power of India's specialty software industry

Made in China 2025 

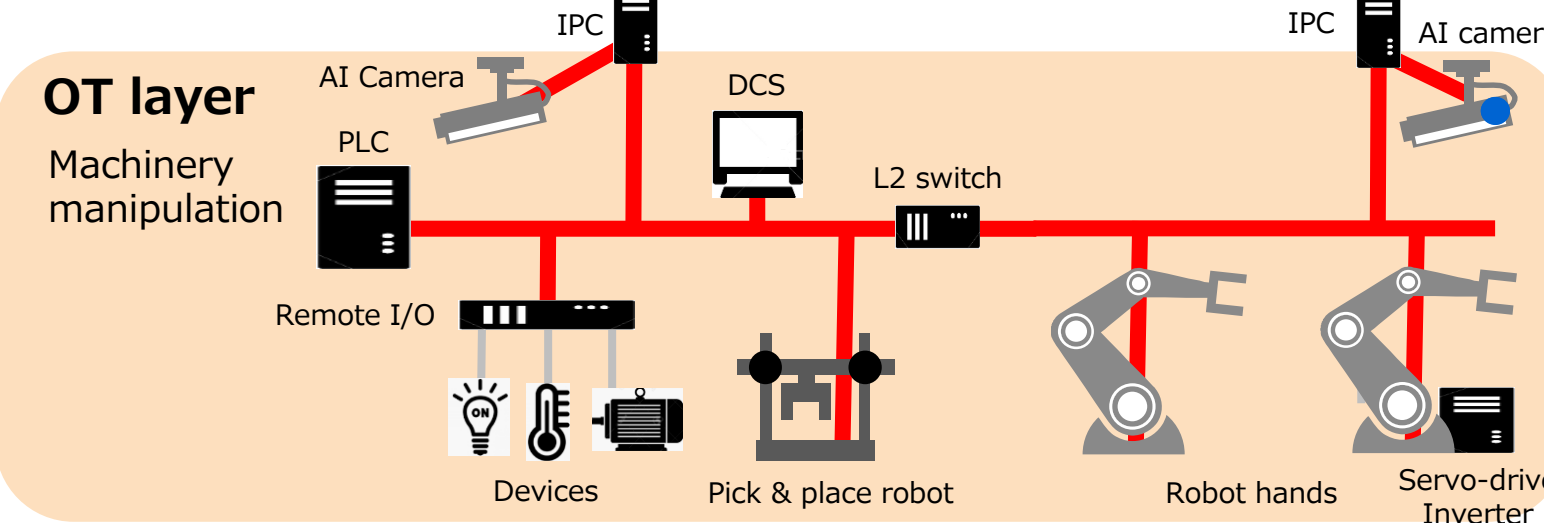
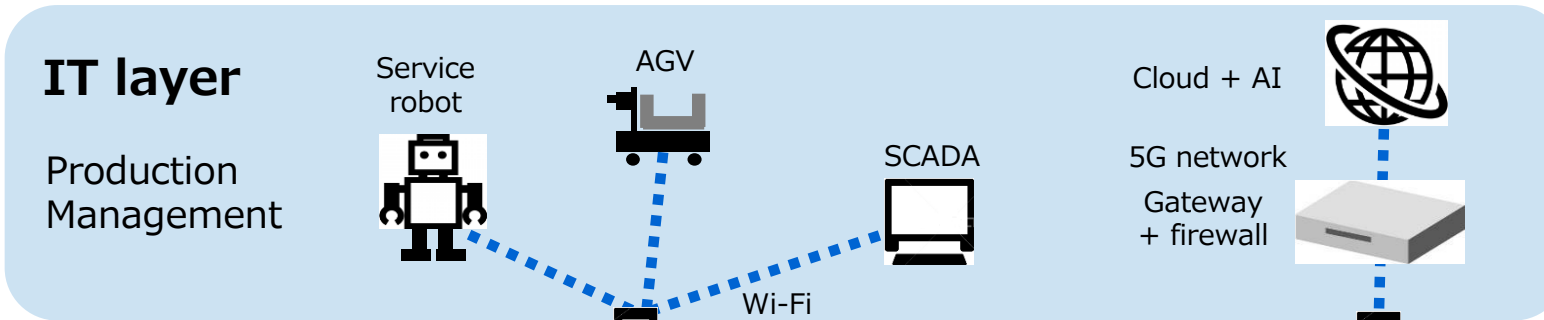
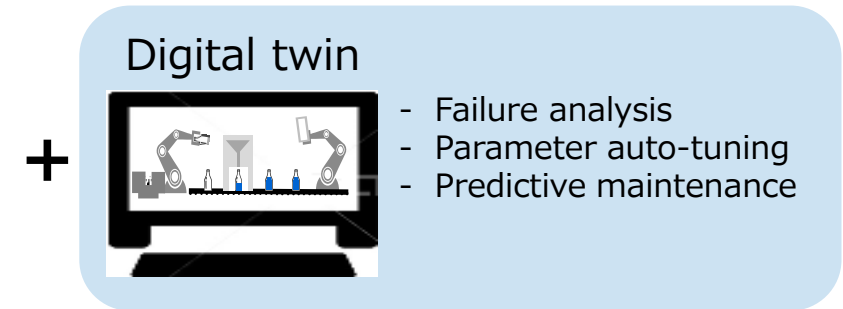
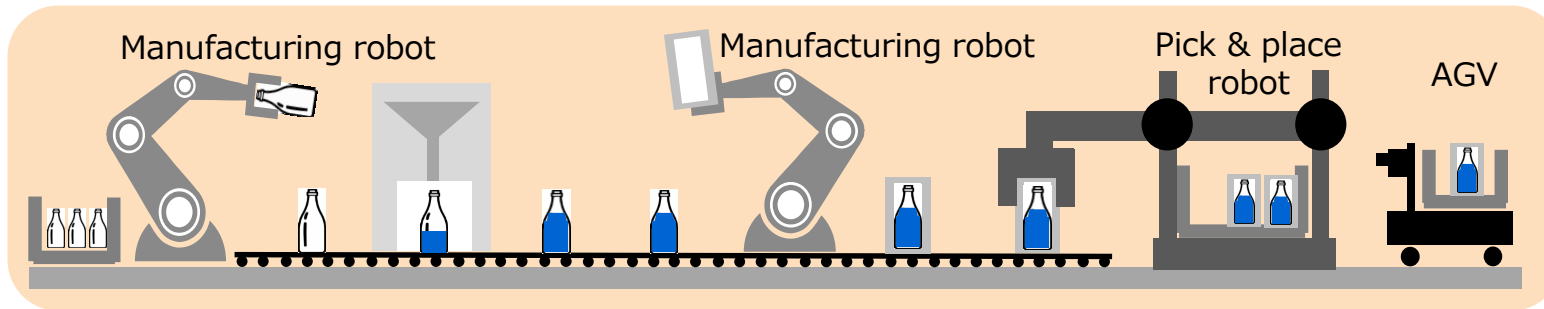
- Withdrawing from simple labor-intensive manufacturing, convert to advanced manufacturing powerhouse as much as developed countries

Robot Revolution Initiative (RRI) 

- **IVI** (Industrial Value-chain Initiative)
- **IoT Acceleration Consortium**



Smart Factory ≈ Operation Tech. + Information Tech.

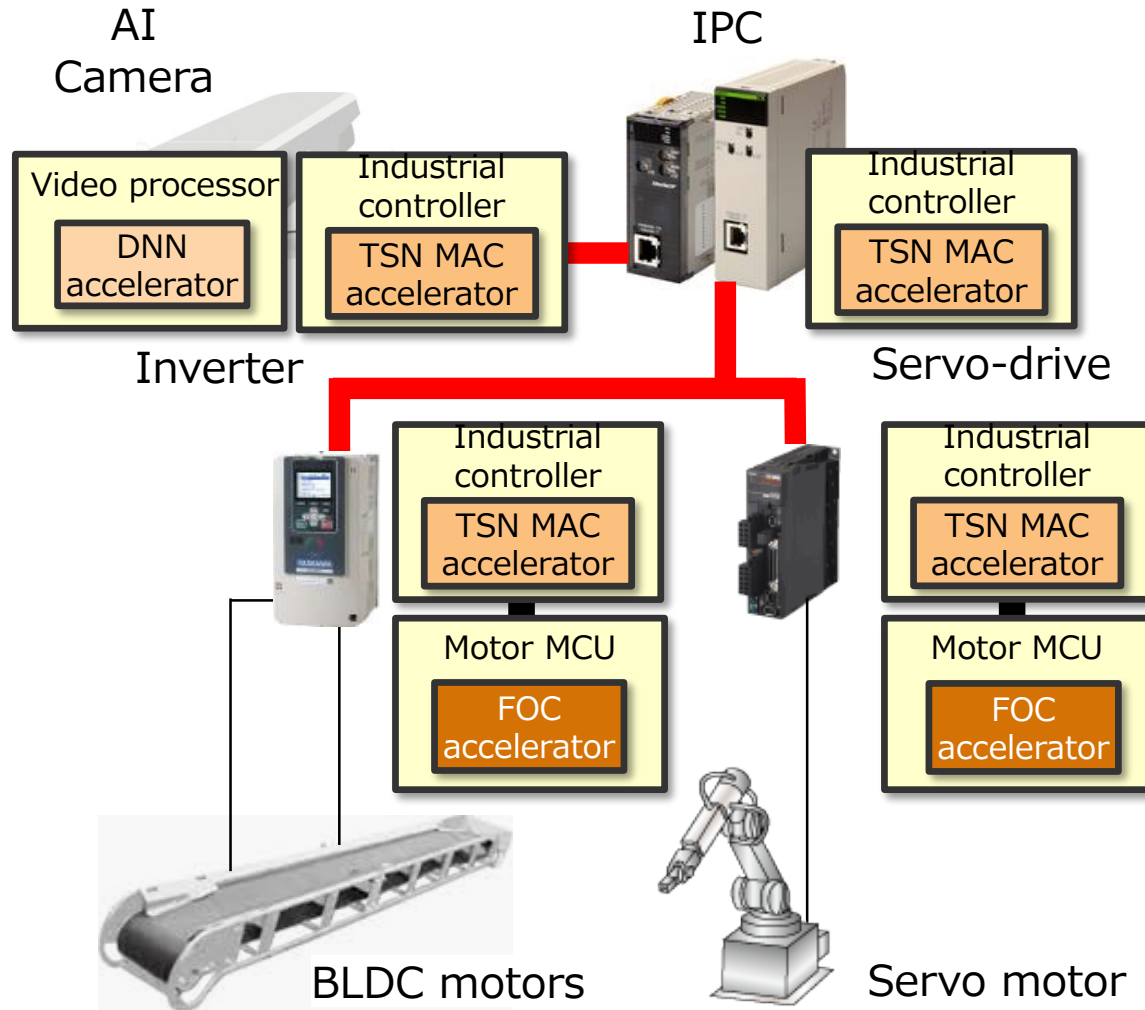


Millisecond time scale

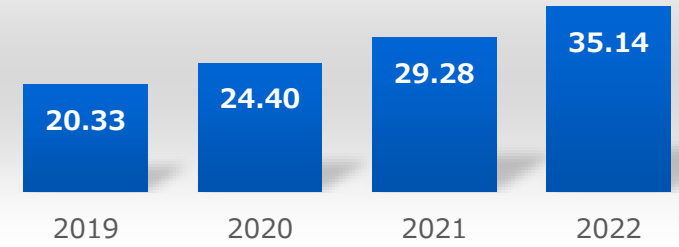
Edge computing

Microsecond time scale

Three target applications in Smart Factory



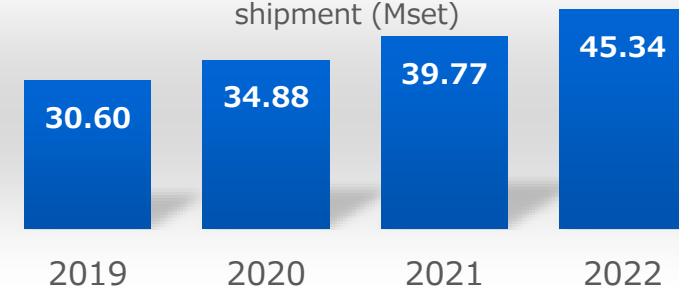
WW AI-camera shipment (Mset)



WW IPC shipment (Mset)



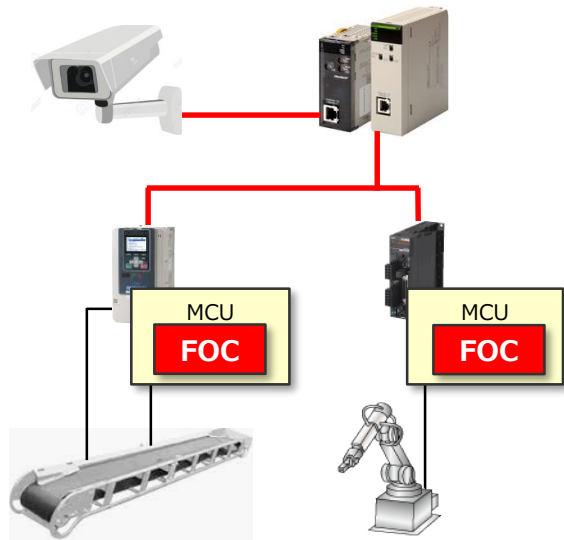
WW FA robot/servo-drive/inverter shipment (Mset)



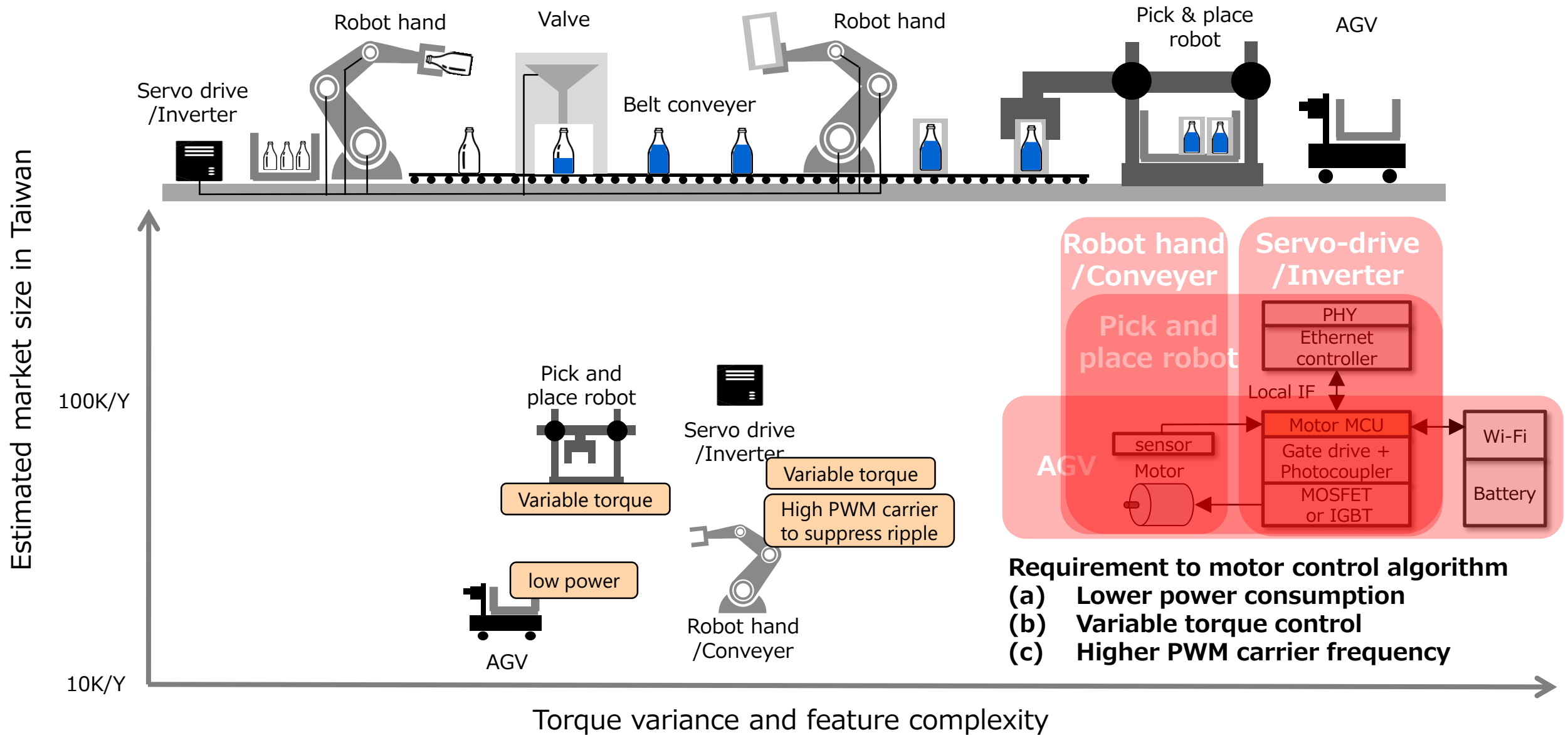
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<https://www.mckinsey.com/industries/technology-media-and-telecommunications/our-insights/new-demand-new-markets-what-edge-computing-means-for-hardware-companies>, <https://www.dri.co.jp/auto/report/mam/mamse4988.html>,
https://www.meti.go.jp/medi_lib/report/H29FY/000671.pdf + Toshiba own survey

02

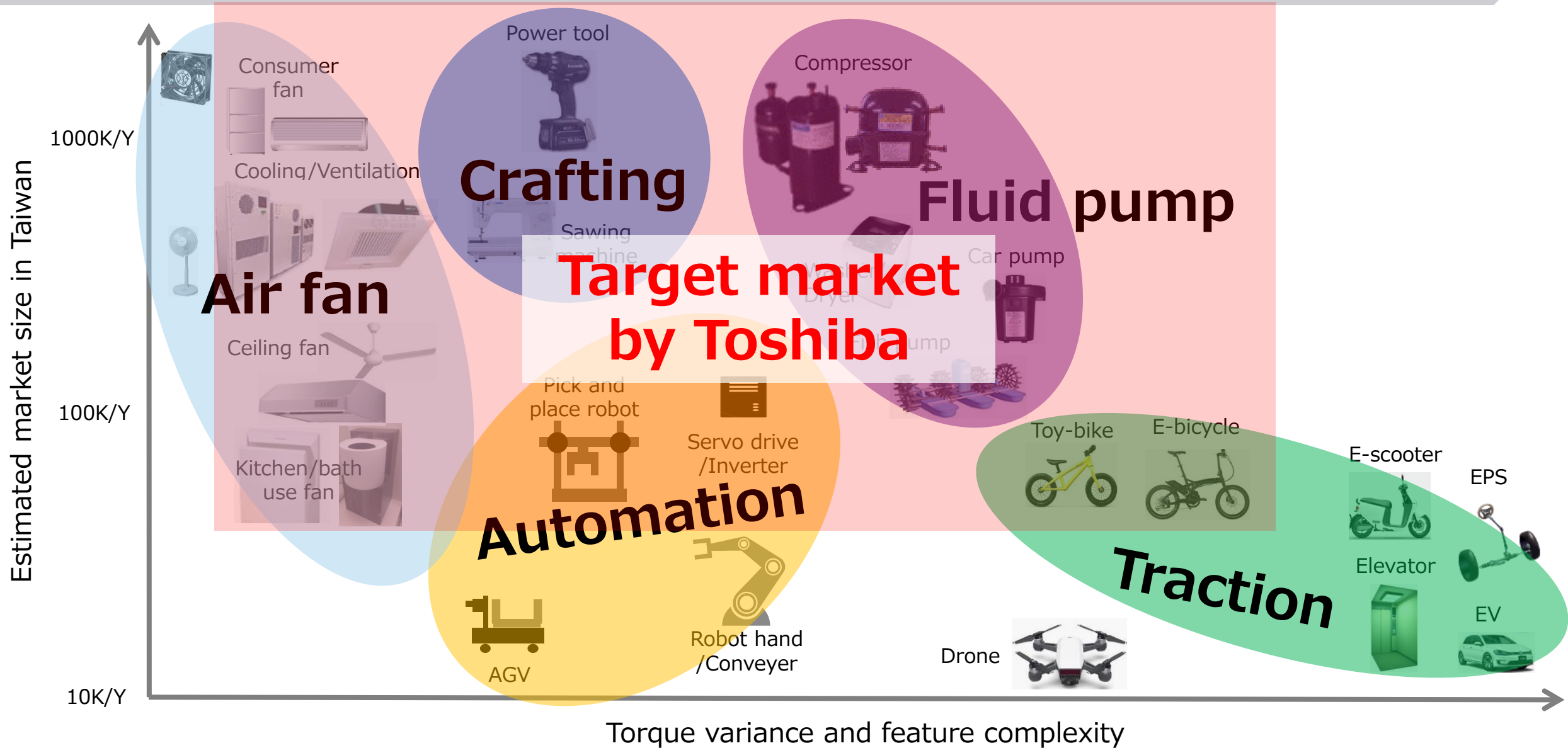
工業4.0加速器I “FOC accelerator”



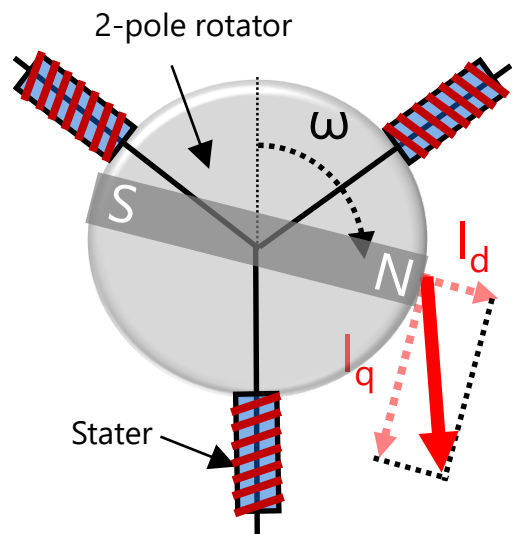
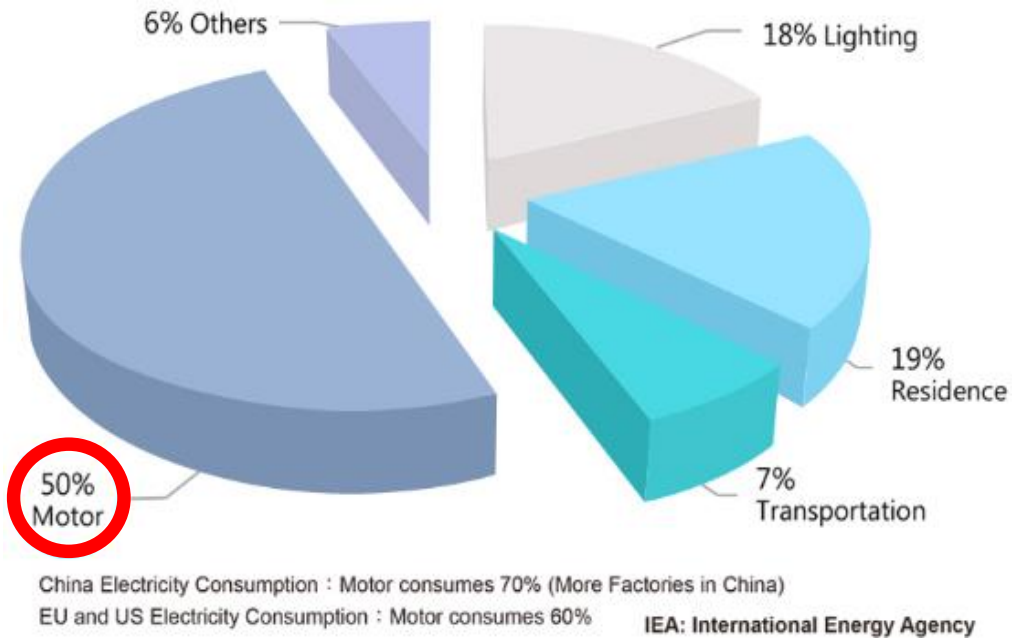
FA market in Taiwan



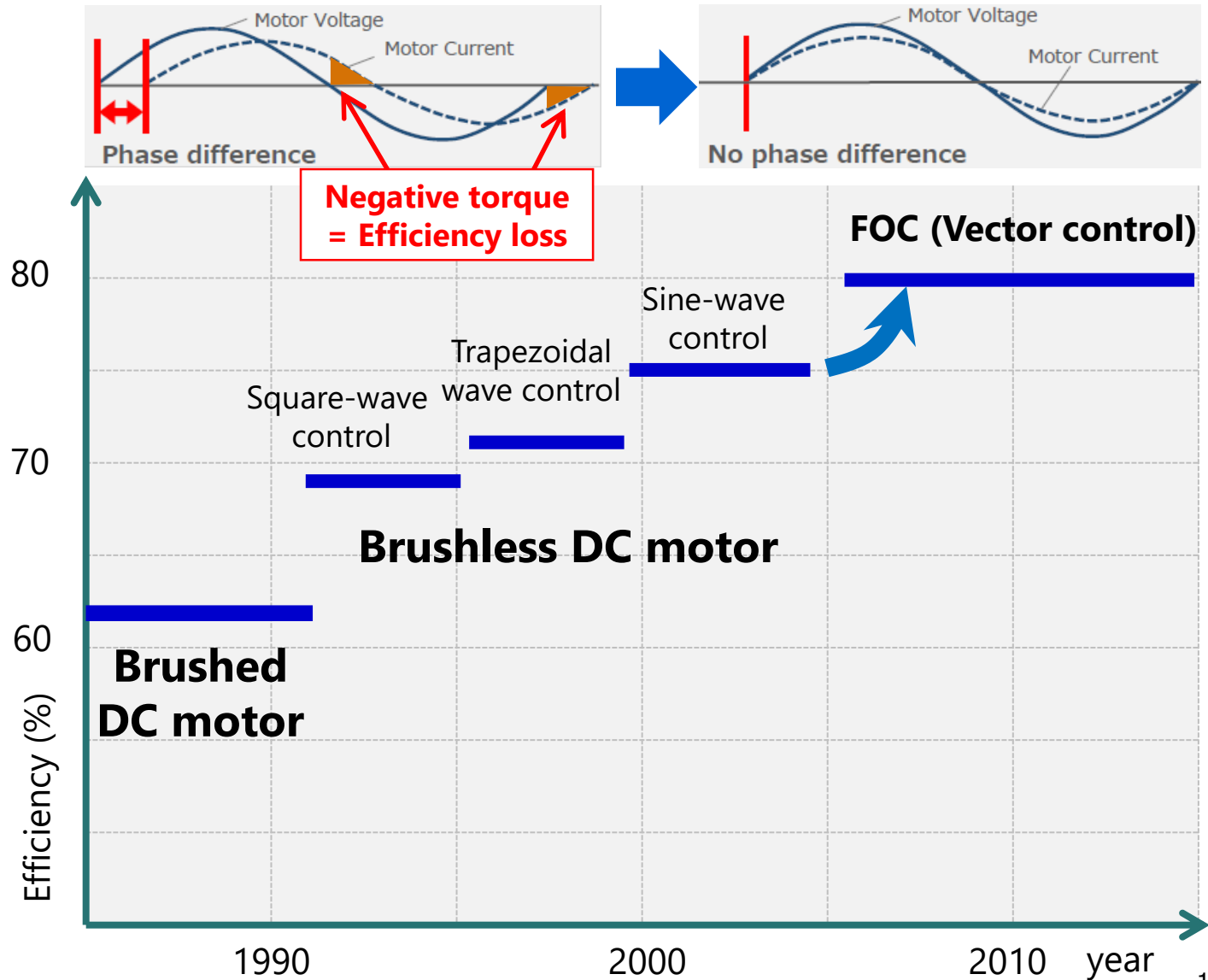
Motor applications in Taiwan



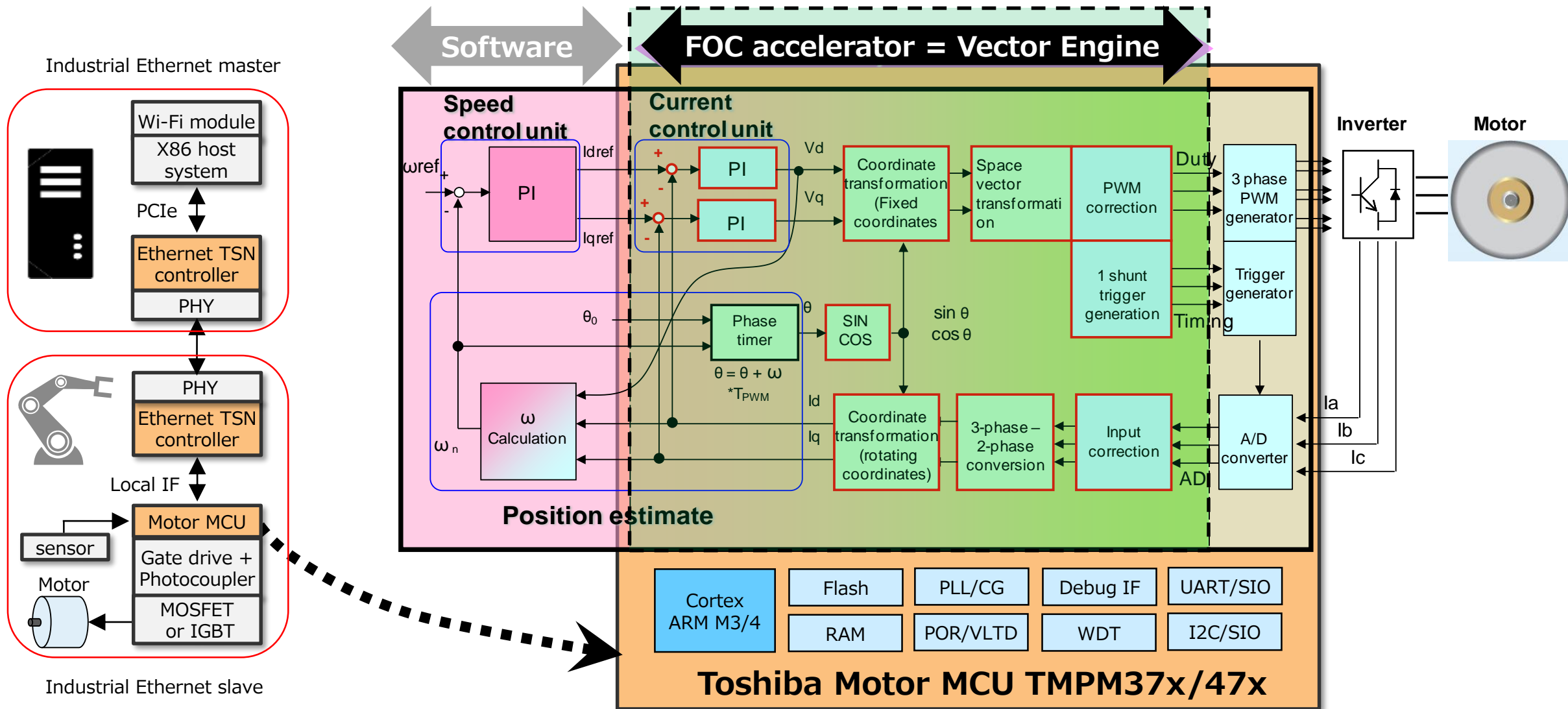
FOC = Field of Control to achieve low-power and torque control



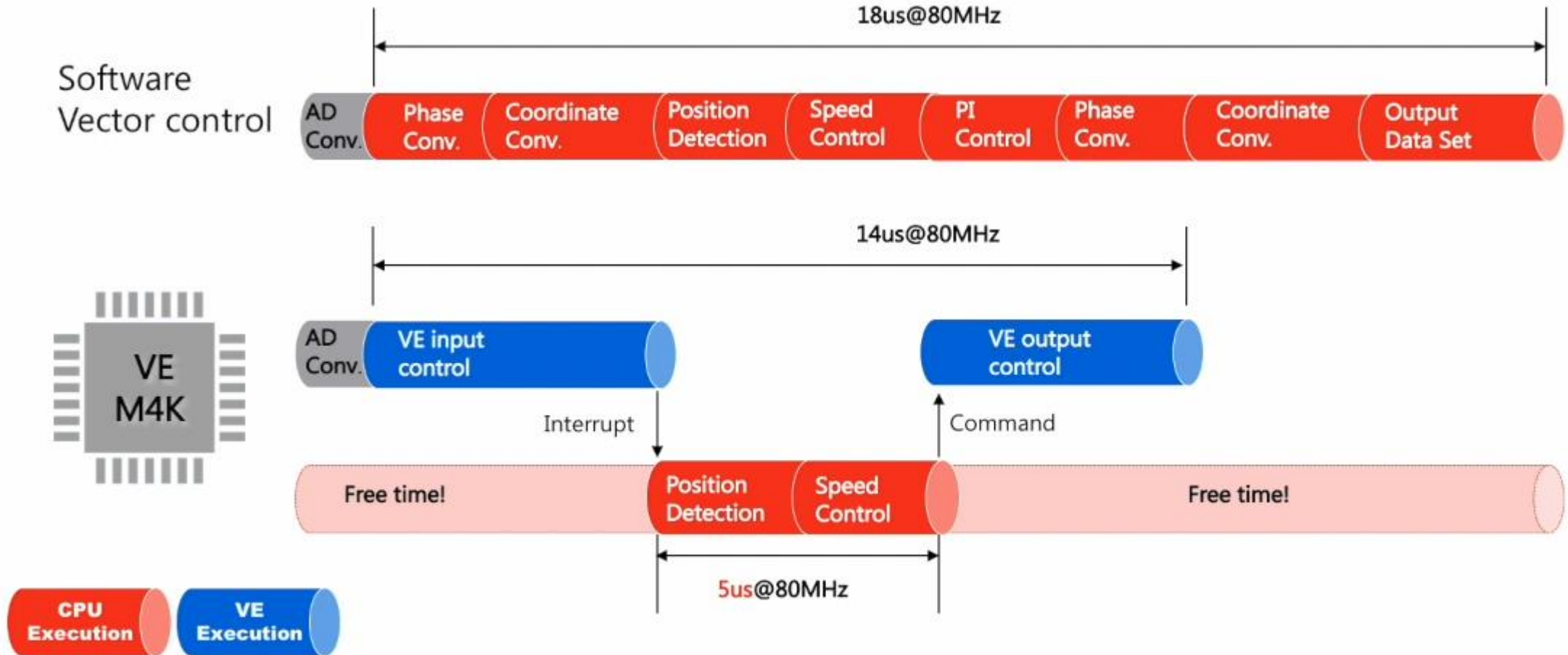
FOC method objective
 $I_d \rightarrow 0$



FOC accelerator = Hardware IP to speed up FOC computation

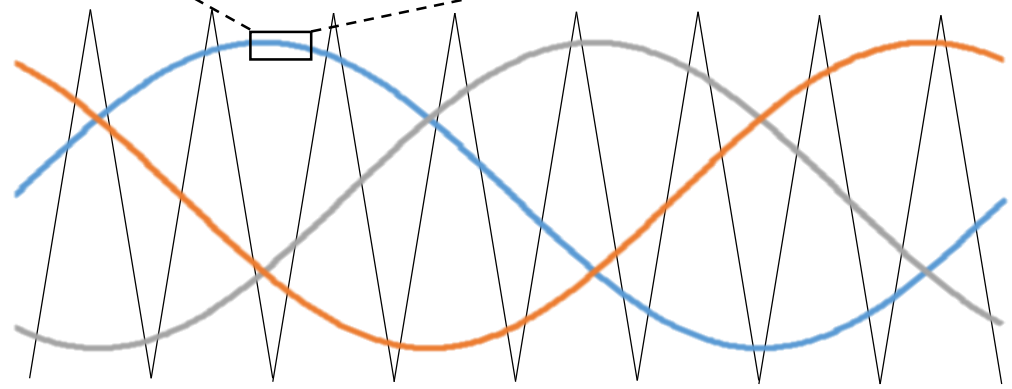
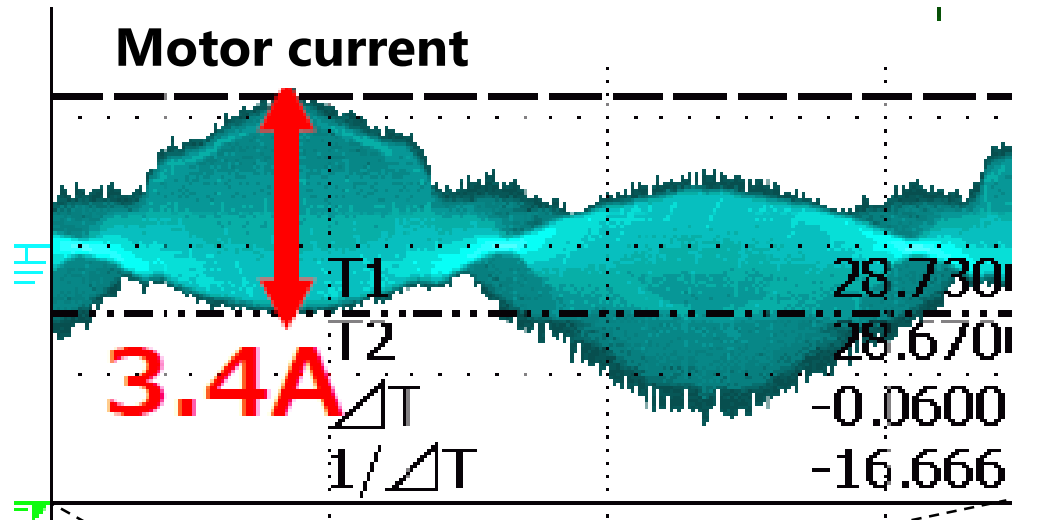


Performance Benefit of FOC accelerator

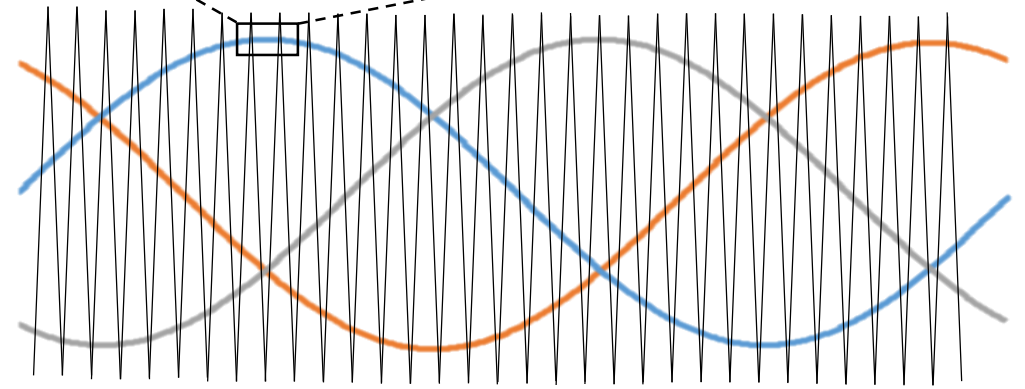
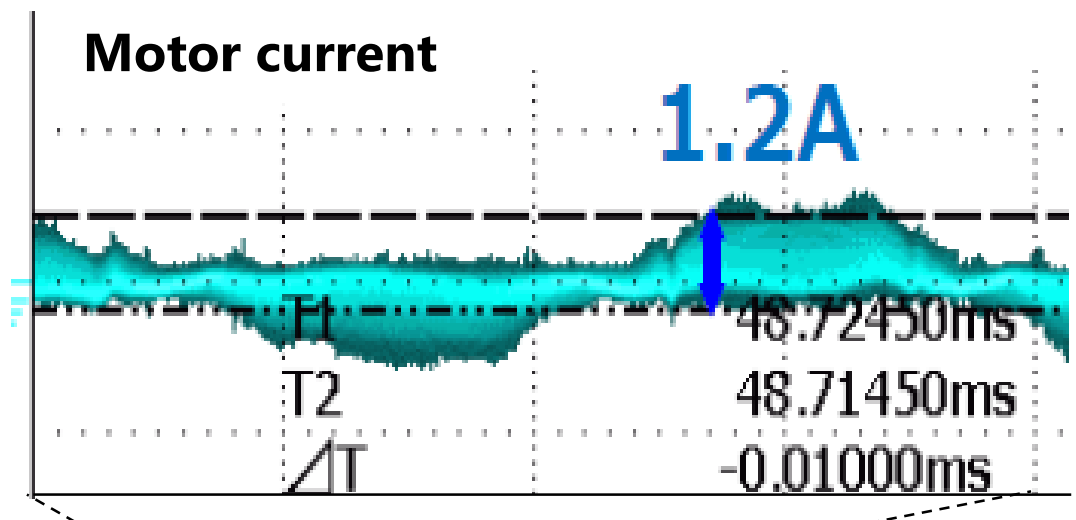


The Vector Engine reduces CPU load and speeds up processing time

Ripple Reduction Benefit of FOC accelerator



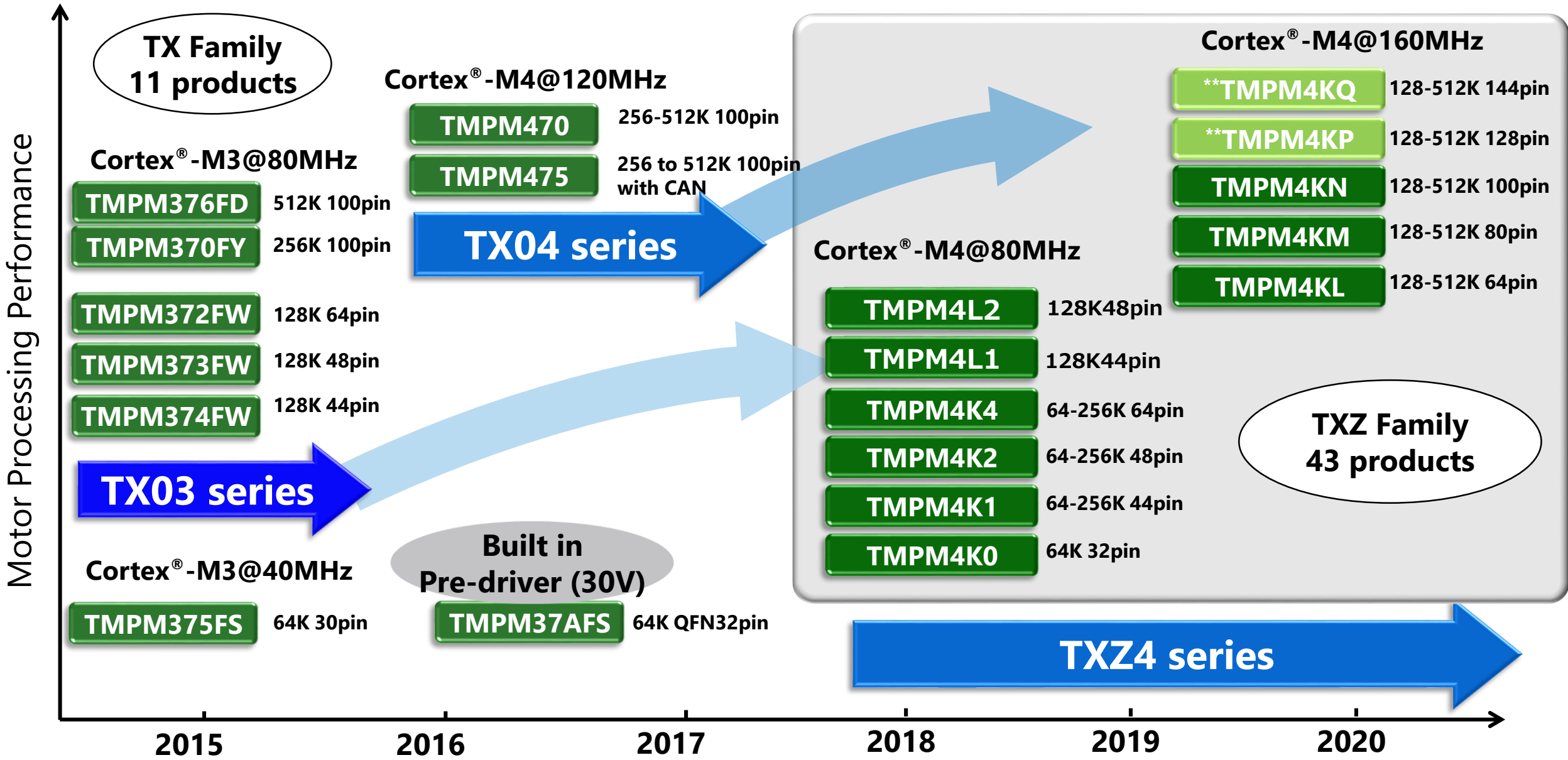
PWM frequency = 16KHz



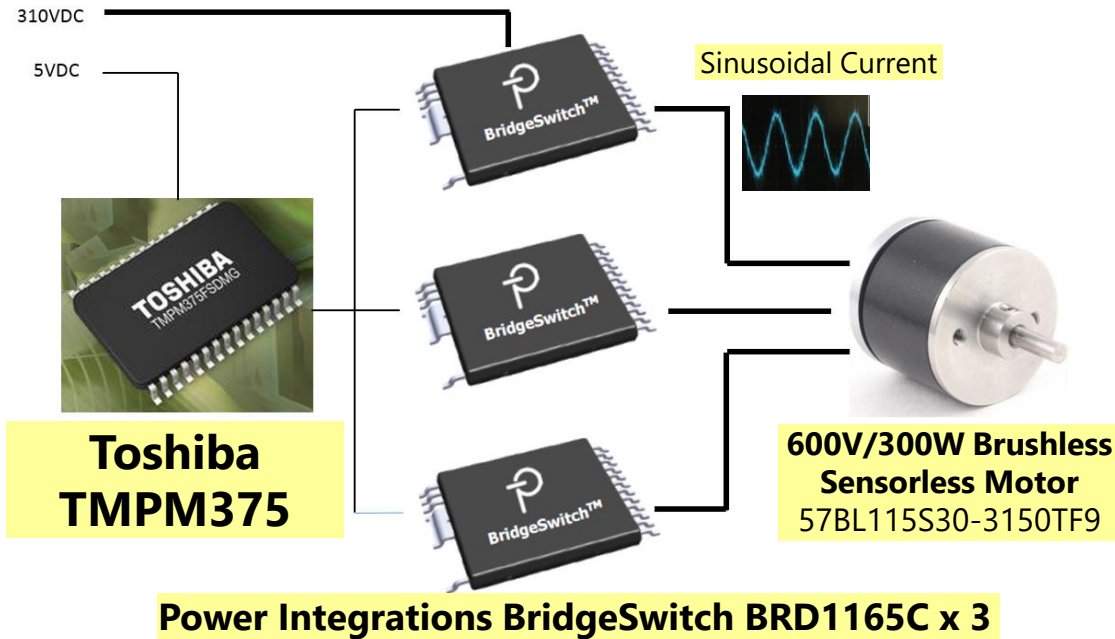
PWM frequency = 100KHz

FOC accelerator MCU Roadmap

MP
Under develop



DER-653: Motor MCU reference solution

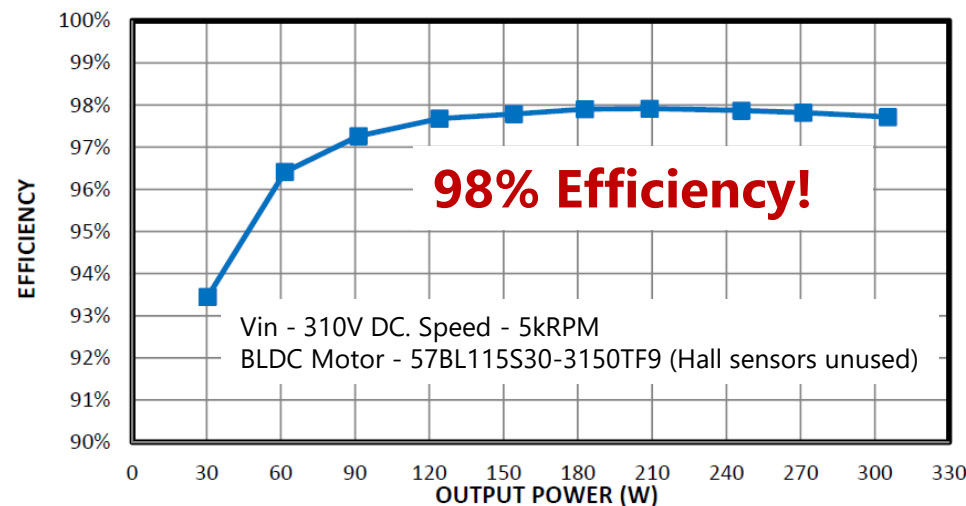
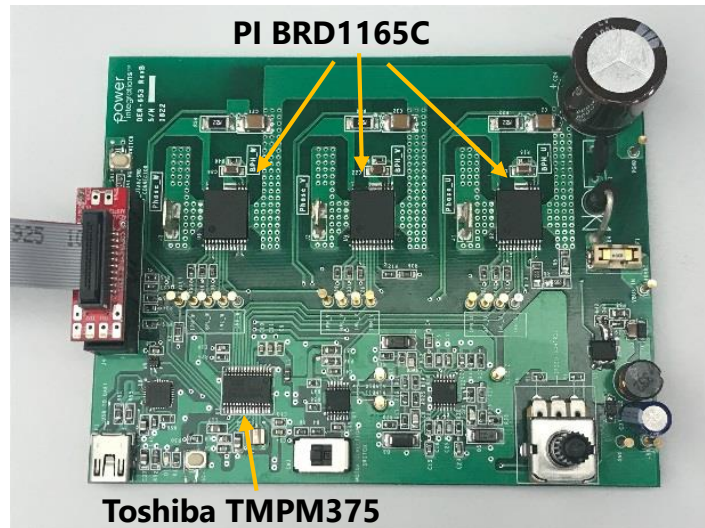


Applications

- Sensor-less/sensor FOC inverter system up to 300W motor

Value Propositions

- Supports 310V/300W/5kRPM Brushless Sensorless Motors
- 98% Efficiency, No Heatsink, Reduce PCB HotSpots
- Ultra Quiet and Virtually No Vibration Sinewave Drive
- FOC Accelerator Frees Up CPU of 73% Bandwidth
- Provides Full Motor Torque Capability Even at Low Speed
- Minimum BOM Solution



DER-653 Solution Demo System

Motor parameter calculation tool
(Developed by 3rd party JAC)

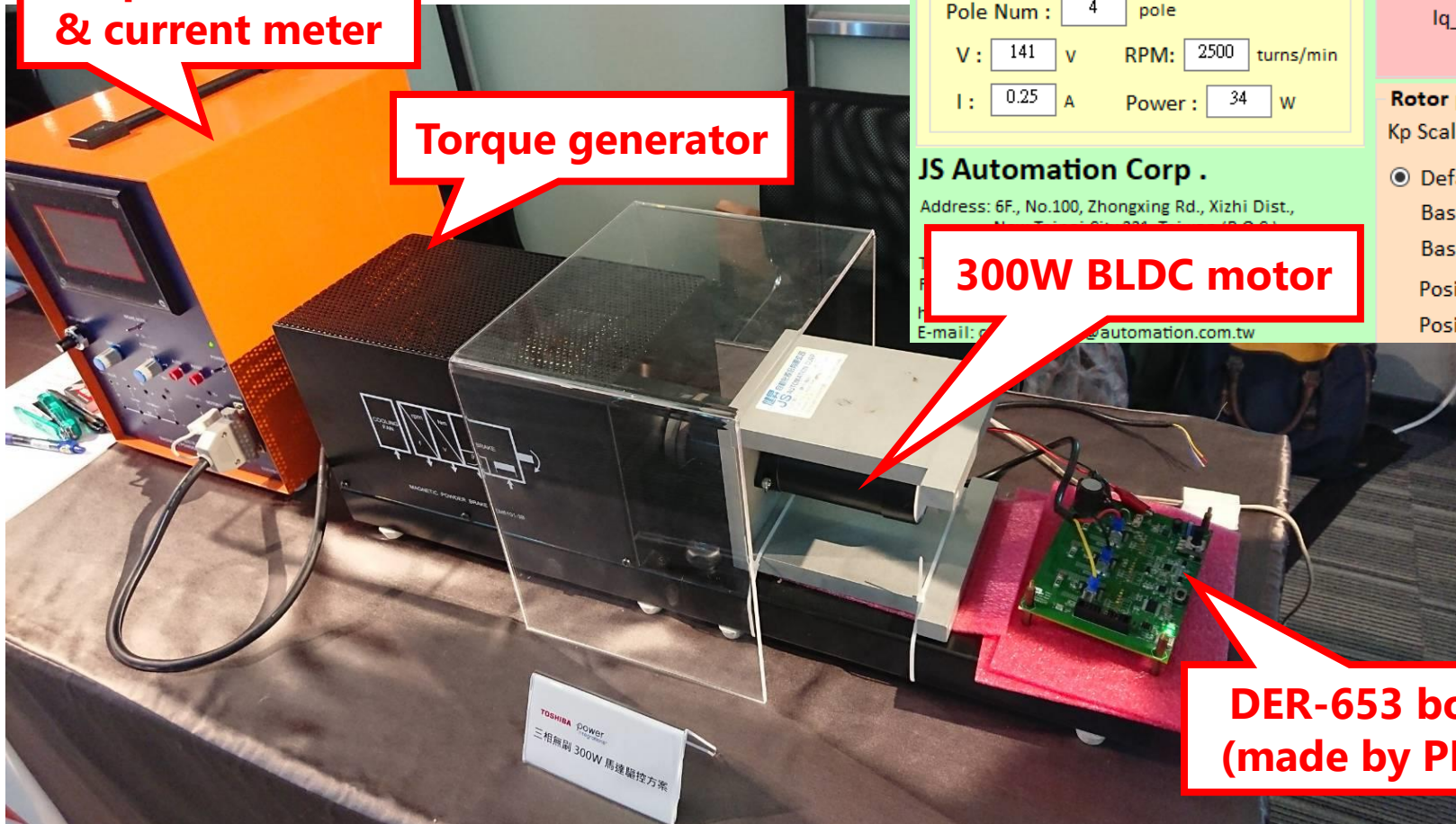
Torque controller
& current meter

Torque generator

300W BLDC motor

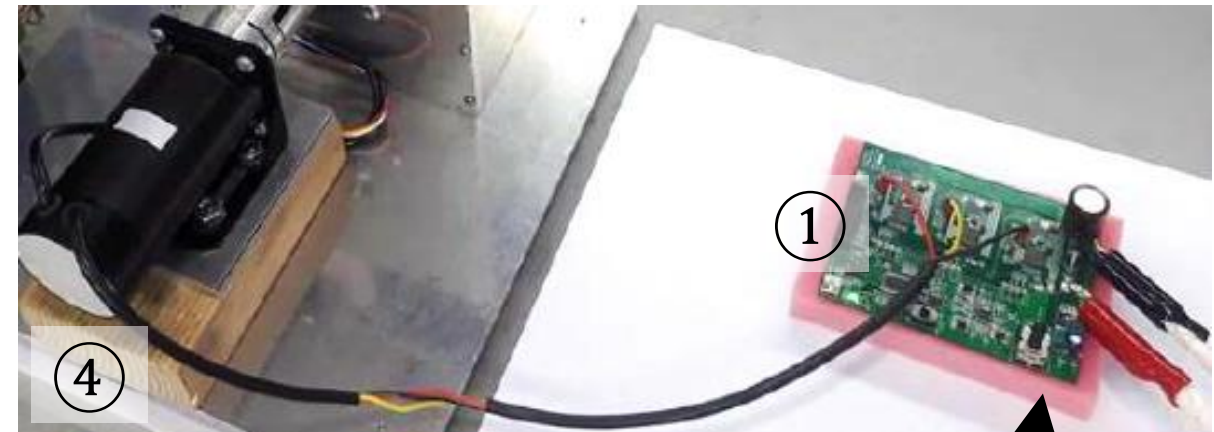
DER-653 board
(made by PI US)

Motor Parameters Module Name : <input type="text" value="Module_name"/> DQ Module Parameters R : <input type="text" value="41"/> Ohm Ld : <input type="text" value="70"/> mH Lq : <input type="text" value="70"/> mH Rated rating Pole Num : <input type="text" value="4"/> pole V : <input type="text" value="141"/> V RPM: <input type="text" value="2500"/> turns/min I : <input type="text" value="0.25"/> A Power : <input type="text" value="34"/> W	Current Control Parameters ω_n : <input type="text" value="660"/> rad/s ζ : <input type="text" value="1"/> <input checked="" type="radio"/> Default <input type="radio"/> Manual Keyin Id_Kp = 51.4 V/A Id_Ki = 30492 V/As Iq_Kp = 51.4 V/A Iq_Ki = 30492 V/As <input type="button" value="Calc"/>	Speed Loop Parameters Motor Working Parameters I : <input type="text" value="0.25"/> A RPM : <input type="text" value="2500"/> turns/min Power : <input type="text" value="34"/> W Rotor D : <input type="text" value="5"/> cm ζ : <input type="text" value="2.5"/> Rotor L : <input type="text" value="3"/> cm ω_n : <input type="text" value="40"/> rad/s J_Load : <input type="text" value="0"/> % <input checked="" type="radio"/> Default <input type="radio"/> Manual Keyin Speed_Kp = 0.02761 A/Hz Speed_Ki = 0.22089 A/Hz <input type="button" value="Calc"/>
JS Automation Corp . Address: 6F., No.100, Zhongxing Rd., Xizhi Dist., New Taipei City, 221, Taiwan (R.O.C.) E-mail: js@jsautomation.com.tw		
Rotor position control Kp Scale : <input type="text" value="1"/> Ki Scale : <input type="text" value="1"/> <input checked="" type="radio"/> Default <input type="radio"/> Manual Keyin Base_Kp = 0.66269 Hz/V Base_Ki = 0.00331 Hz/Vs Position_Kp = 0.66269 Hz/V Position_Ki = 0.66269 Hz/Vs		



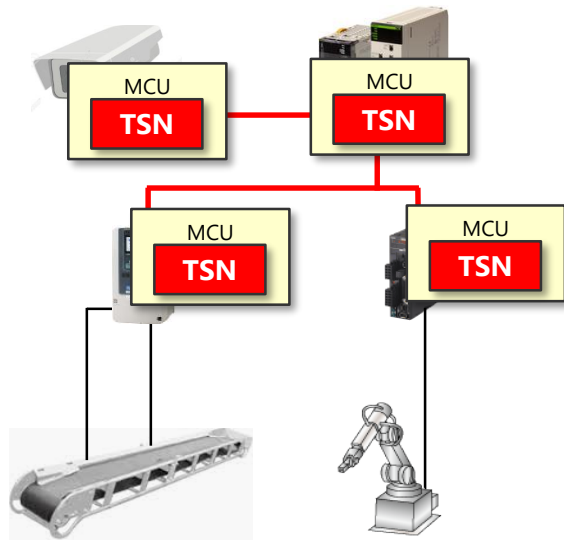
Half-day DER-653 solution workshop announcement

Item	Description
Date & Time	2019/12/26 (Thu) 14:00-17:00
Location	Toshiba Taiwan office 台北市南京東路3段168號4樓 L1 & L2
Fee	Free
Target attendees	Motor system EE engineers Software engineers
Quota	12 people
Hands-on activities	<ul style="list-style-type: none"> ① DER-653 board usage ② FOC tooling usage ③ FW build/debug with EWARM ④ Motor operation using 300W BLDC
Items prepared by attendees	<ul style="list-style-type: none"> Ⓐ Notebook PCs Ⓑ EWARM pre-installation (one-month evaluation version)
Contact window	cindy2.kuo@toshiba.co.jp

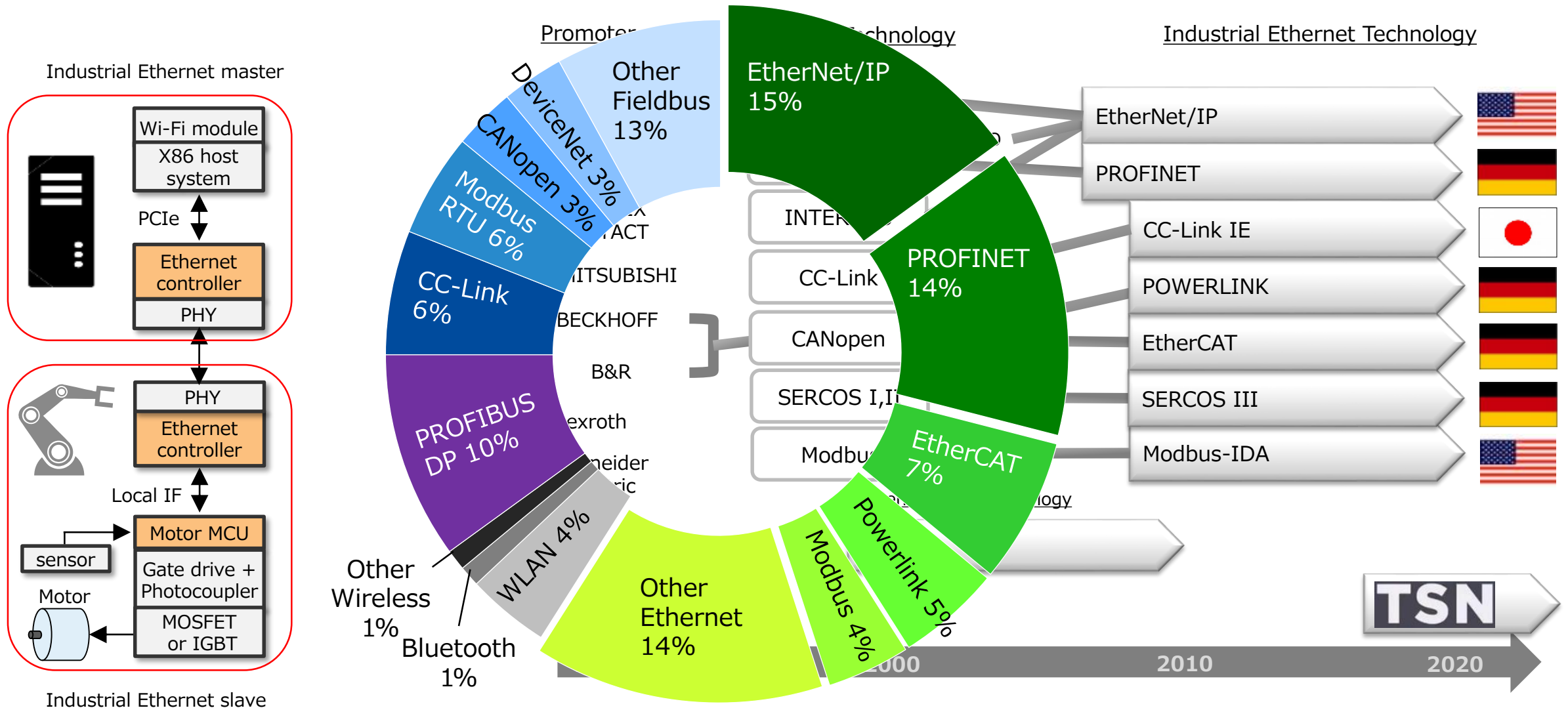


03

工業4.0加速器II “TSN accelerator”

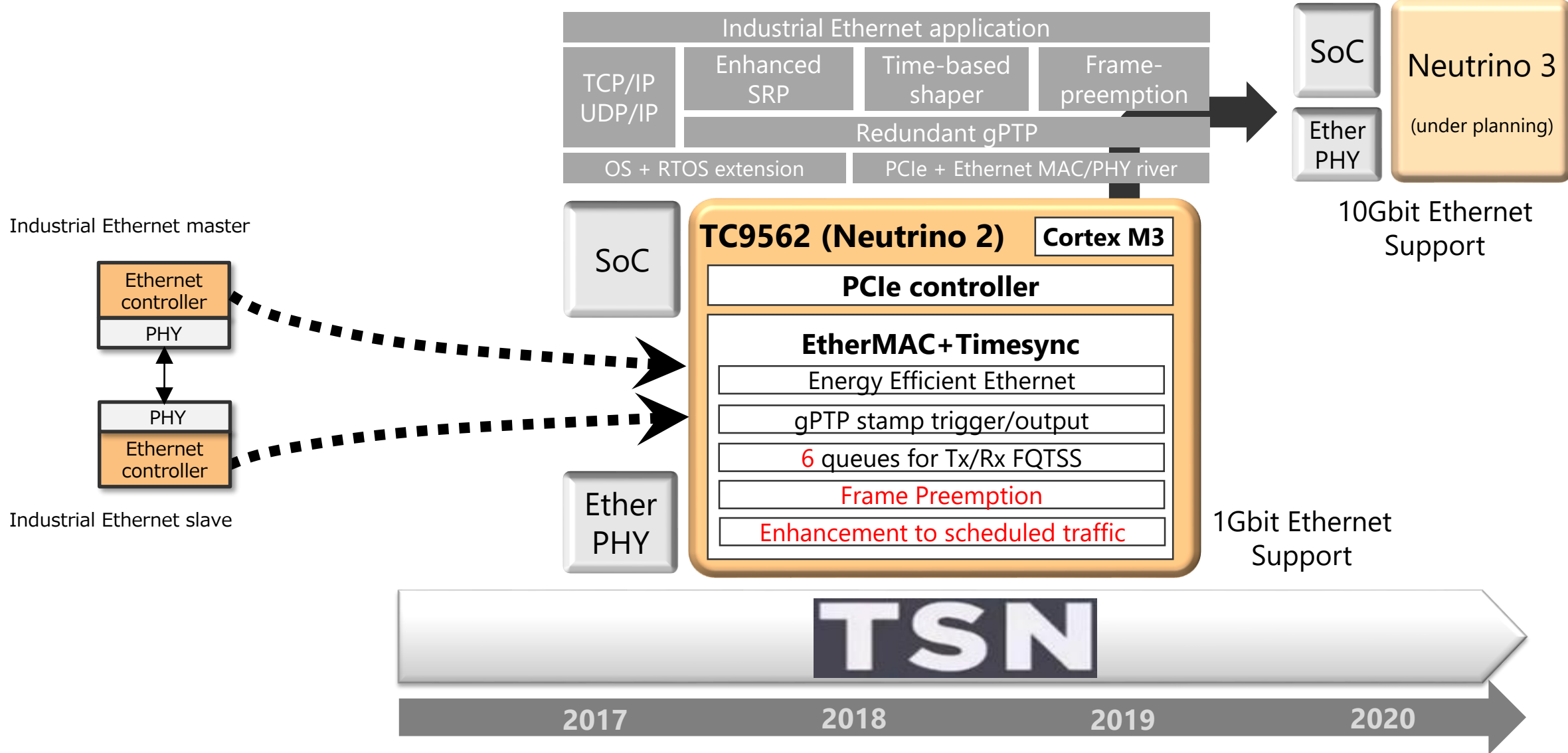


Industrial Ethernet market



<https://www.hms-networks.com/news-and-insights/news-from-hms/2019/05/07/industrial-network-market-shares-2019-according-to-hms>

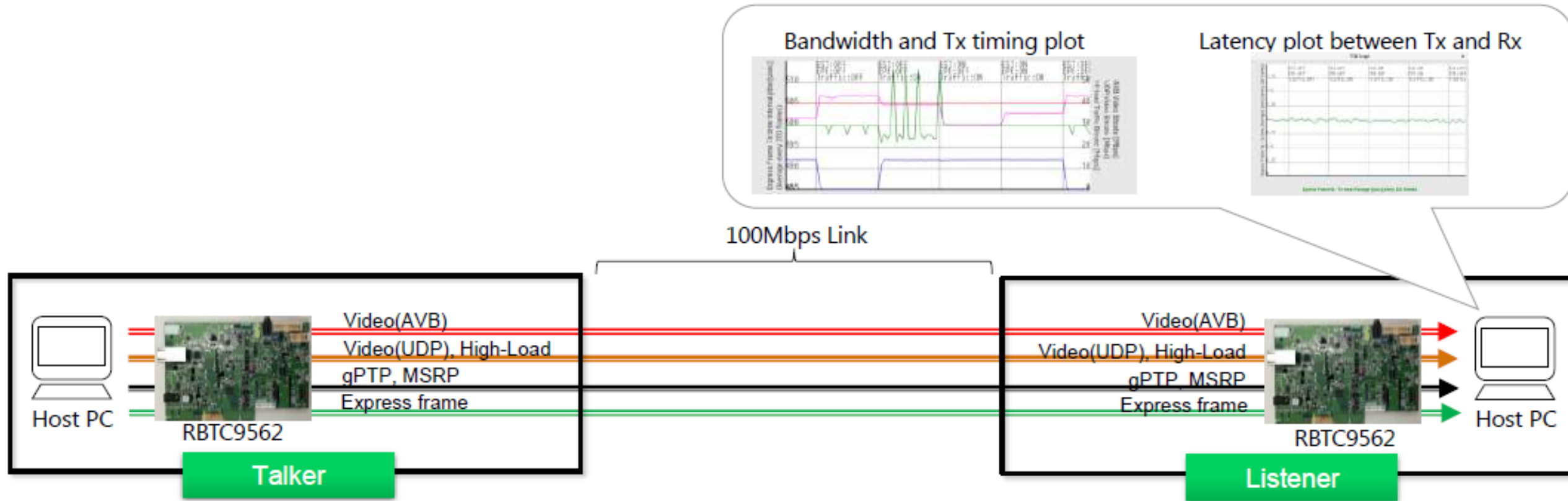
Ethernet TSN bridge IC product lineup



Neutrino is a pet name of Toshiba Ethernet TSN bridge products

Neutrino 2 hardware function test suite and demo

TSN test application is for confirming **EST (Enhancements to Scheduled Traffic)** & **FPE (Frame preemption)** features. This application transmit the Express frame of 214 Byte every 500 usec from Talker, and periodic switching EST & FPE on/off with every 10 sec.

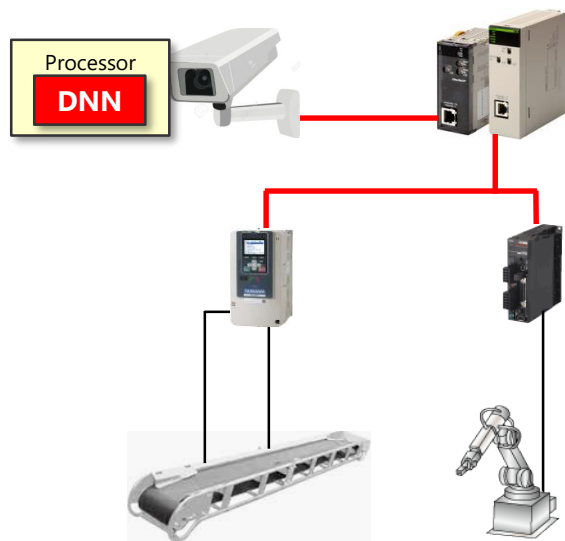


The following features can be confirmed with this application.

- Toggle EST on/off to confirm **Express frame transmit complete timing**
- Validate FPE improvement of **Total bandwidth**



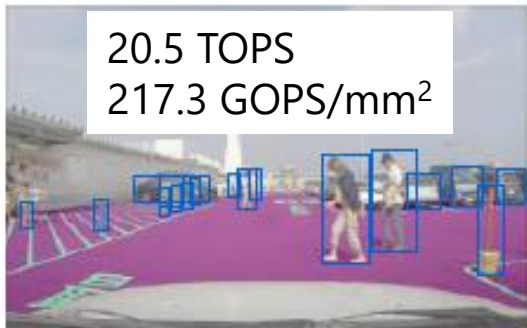

04

工業4.0加速器III “DNN accelerator”

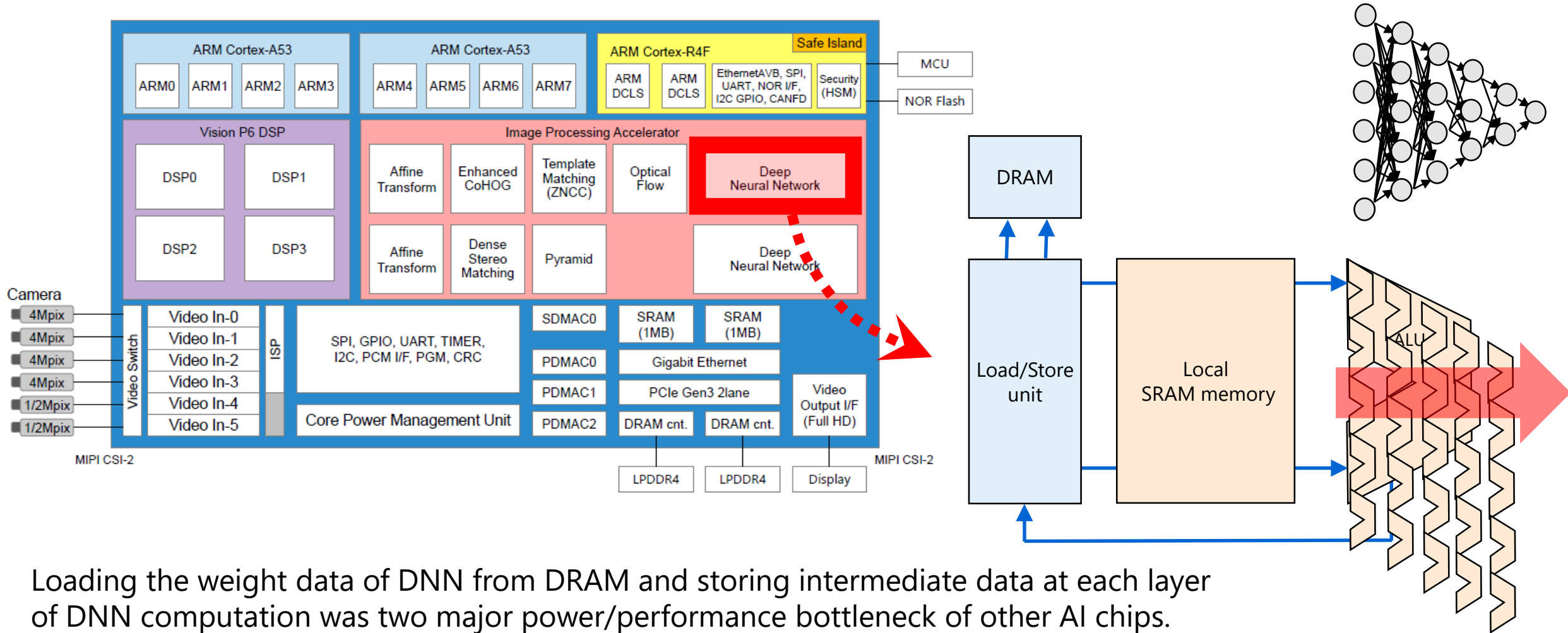


Visconti 5 is Toshiba's first AI processor focusing ADAS first



Visconti2/3 (EuroNCAP2016)	Visconti4 (EuroNCAP2018)	Visconti5 (EuroNCAP2020)	Next generation (EuroNCAP202x)
Mono Front/Rear sensing (Daytime)	Mono Front/Rear sensing (Daytime & Night)	Mono/Stereo + Milli wave Front sensing 360° surround sensing Auto parking	Autonomous driving (recognition + judgement) Mono + Milli wave or LiDAR Front sensing Mono + Milli wave 3D view + surround sensing Auto parking
	 1.9 TOPS 2.6 GOPS/mm ²	 20.5 TOPS 217.3 GOPS/mm ²	

Visconti 5 architecture



Loading the weight data of DNN from DRAM and storing intermediate data at each layer of DNN computation was two major power/performance bottleneck of other AI chips.

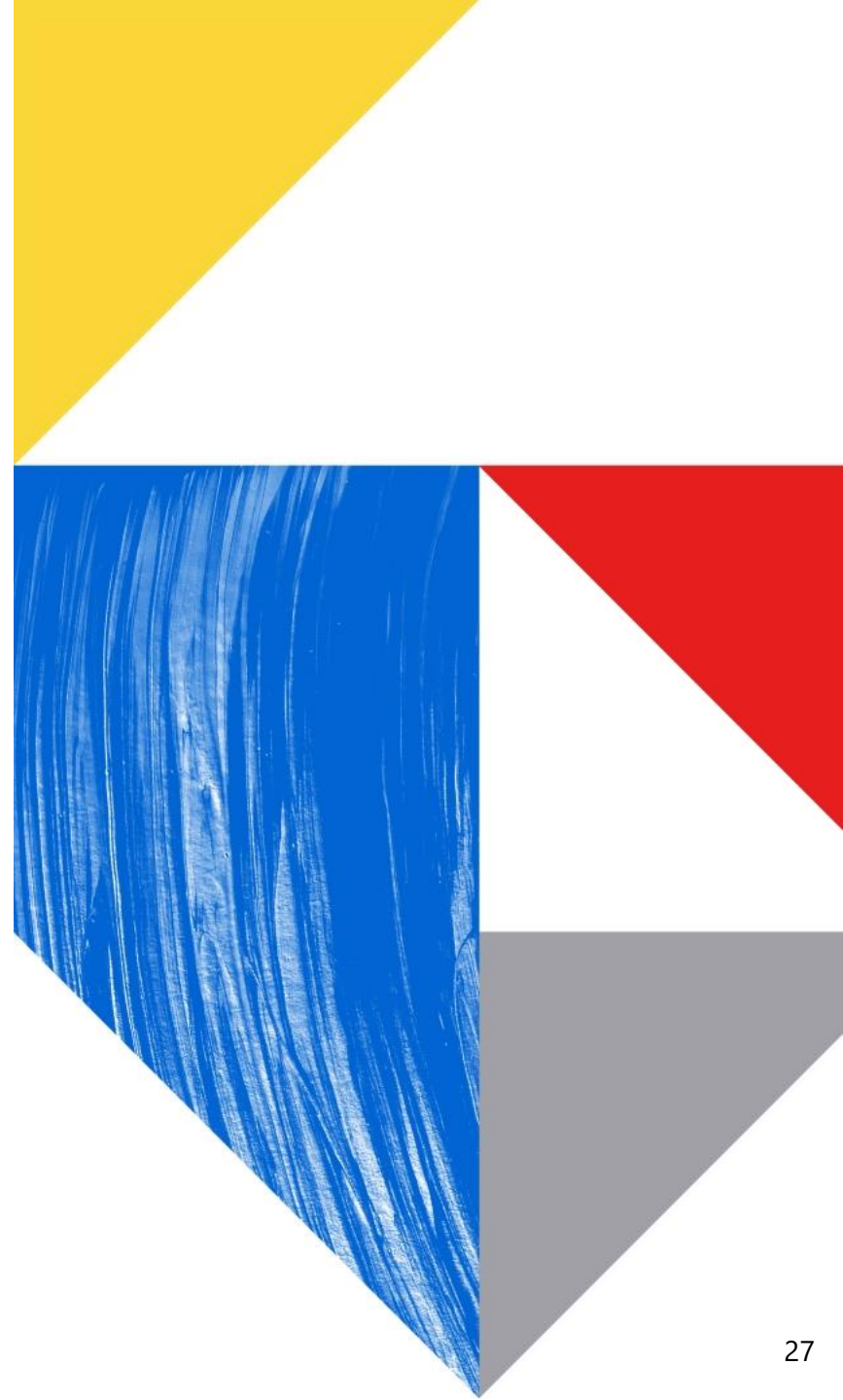
- Our DNN accelerator
- (1) Adds SRAM to store weight data,
 - (2) Create 256 ALUs to be able to load weight data for constructing DNN
 - (3) Use pipeline to execute DNN computation sequentially without accessing load/store unit

Deep Neural Network demo video



05

示範平臺



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